

## Advanced manufacturing of dielectric meta-devices

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**Abstract.** Metasurfaces, composed of two-dimensional nanostructures, exhibit remarkable capabilities in shaping wavefronts, encompassing phase, amplitude, and polarization. This unique proficiency heralds a transformative paradigm shift in the domain of next-generation optics and photonics, culminating in the development of flat and ultrathin optical devices. Particularly noteworthy is the all-dielectric-based metasurface, leveraging materials such as titanium dioxide, silicon, gallium arsenide, and silicon nitride, which finds extensive application in the design and implementation of high-performance optical devices, owing to its notable advantages, including a high refractive index, low ohmic loss, and cost-effectiveness. Furthermore, the remarkable growth in nanofabrication technologies allows for the exploration of new methods in metasurface fabrication, especially through wafer-scale nanofabrication technologies, thereby facilitating the realization of commercial applications for metasurfaces. This review provides a comprehensive overview of the latest advancements in state-of-the-art fabrication technologies in dielectric metasurface areas. These technologies, including standard nanolithography [e.g., electron beam lithography (EBL) and focused ion beam (FIB) lithography], advanced nanolithography (e.g., grayscale and scanning probe lithography), and large-scale nanolithography [e.g., nanoimprint and deep ultraviolet (DUV) lithography], are utilized to fabricate high-resolution, high-aspect-ratio, flexible, multilayer, slanted, and wafer-scale all-dielectric metasurfaces with intricate nanostructures. Ultimately, we conclude with a perspective on current cutting-edge nanofabrication technologies.

Keywords: dielectric; meta-devices; meta-optics; metasurface; nanofabrication; nanotechnology; nanophotonics; large/wafer-scale nanofabrication.

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### 1 Introduction

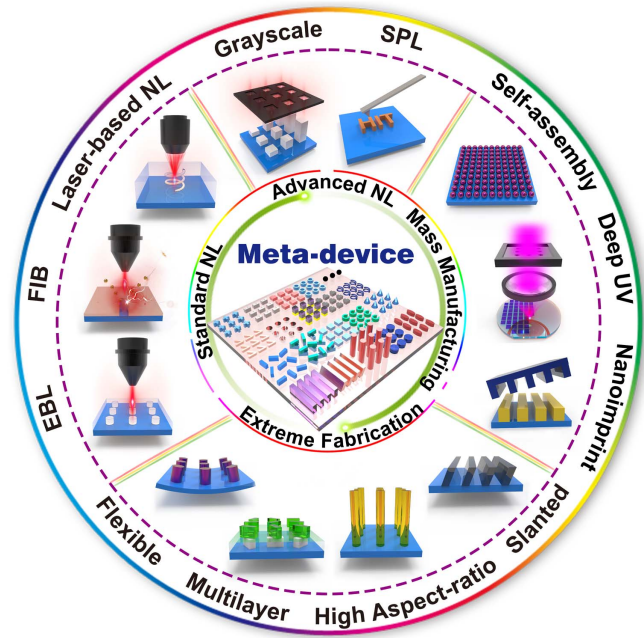
Nanofabrication technology is an exhilarating and swiftly advancing area that can produce exceedingly diminutive structures, operating at scales ranging from 1 to 100 nm. This innovative technology has brought about a revolution in the field of nanophotonics, particularly within the realm of metasurfaces. Metasurfaces are two-dimensional (2D) metamaterials that

consist of ultrathin subwavelength structure arrays that are so-called meta-atoms<sup>[1–10]</sup>. Through specific design and artificial arrangement, metasurfaces demonstrate a remarkable ability to manipulate the wavelength, amplitude, phase, and polarization of electromagnetic waves. This versatility finds applications in diverse fields such as metalenses<sup>[11–20]</sup>, metaholograms<sup>[21–26]</sup>, structural colors<sup>[27–34]</sup>, optical vortex generation<sup>[35–40]</sup>, spectrometers<sup>[41–44]</sup>, imaging<sup>[44–47]</sup>, sensing<sup>[48–55]</sup>, and beam manipulation<sup>[56–61]</sup>. Metasurfaces can be broadly categorized into two types based on the materials of their unit cells: plasmonic<sup>[62,63]</sup> and dielectric<sup>[64]</sup> metasurfaces. Initial studies predominantly

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focused on plasmonic metasurfaces, consisting of planar arrays of metallic nanoantennas, typically made of noble metals like gold or silver. These structures exhibit plasmonic resonance when illuminated, driven by the collective oscillation of free electrons at the metal–dielectric interface, known as localized surface plasmon resonance (LSPR)<sup>[65]</sup>, wherein the electromagnetic field couples to the electron plasma of a conductor. However, the plasmonics faces inherent ohmic losses associated with the free electron response, and incompatibility with complementary metal–oxide semiconductor (CMOS) fabrication processes poses a significant challenge, limiting both the optical device’s working and fabrication efficiency, which is a determining factor for whether metasurfaces can achieve further application. To address this limitation, the all-dielectric resonator has emerged as a promising alternative. Comprising high refractive index and low-loss dielectric nanoblocks, such as titanium dioxide (TiO<sub>2</sub>)<sup>[66–72]</sup>, silicon (Si)<sup>[73,74]</sup>, silicon nitride (Si<sub>3</sub>N<sub>4</sub>)<sup>[75–77]</sup>, and gallium arsenide (GaAs)<sup>[78,79]</sup>, all-dielectric metasurfaces can support electric and magnetic multipole modes based on Mie resonance theory with negligible absorption loss. This characteristic enables efficient manipulation of optical responses and the creation of high-efficiency photonic devices. The Mie resonance in dielectric meta-atoms depends on the material and dimensions, allowing convenient engineering of scattering characteristics<sup>[80]</sup>. The tuning of resonances and their spectral positions is achieved by finely tailoring the geometry parameters of nanoparticles. Therefore, the tolerance and roughness control in nanofabrication play a crucial role in determining the performance of dielectric optical devices, especially in achieving resonances with ultranarrow linewidths, such as bound states in the continuum (BICs)<sup>[81–87]</sup>.

For the precise production of dielectric metasurface nanoblocks, various established nanofabrication techniques have been proposed, such as electron beam lithography (EBL)<sup>[88–91]</sup>, focused ion beam (FIB) lithography<sup>[92]</sup>, and laser-based nanomanufacturing<sup>[93]</sup>, among others. While these advancements are exciting, the challenge of fast, large-scale, and cost-effective metasurface fabrication remains<sup>[94]</sup>. The low cost of mass-production techniques is essential for bringing metasurfaces from laboratories to our daily lives. Efforts have been directed toward reducing the cost and enhancing the throughput of dielectric metasurfaces through methods such as nanoimprinting<sup>[95–97]</sup>, molecular self-assembly<sup>[98]</sup>, roll-to-roll<sup>[99]</sup>, and deep ultraviolet (DUV) lithography<sup>[100]</sup> processes. It is noteworthy that novel nanofabrication techniques have facilitated groundbreaking discoveries, as evidenced by the development of high-aspect-ratio<sup>[101]</sup>, flexible<sup>[102]</sup>, multilayer<sup>[103]</sup>, and slanted nanostructures<sup>[104]</sup>. Numerous reviews have been published to summarize the realm of all-dielectric metasurfaces and their corresponding nanofabrication technologies. These include reviews on the development of dielectric metasurfaces<sup>[64]</sup>, the fundamentals and applications of dielectric metasurfaces<sup>[80,105–107]</sup>, and the fabrication of metasurfaces<sup>[108,109]</sup> encompassing both plasmonic and dielectric materials, among others. However, the majority of these works primarily review fundamental principles and common manufacturing processes, often overlooking the critical aspect of advanced fabrication techniques for dielectric metasurfaces. Given that dielectric metasurfaces are considered one of the most promising platforms for advancing meta-optics toward commercialization, a specific review focusing on the advanced manufacturing of dielectric meta-devices is both necessary and timely. This review comprehensively explores recent

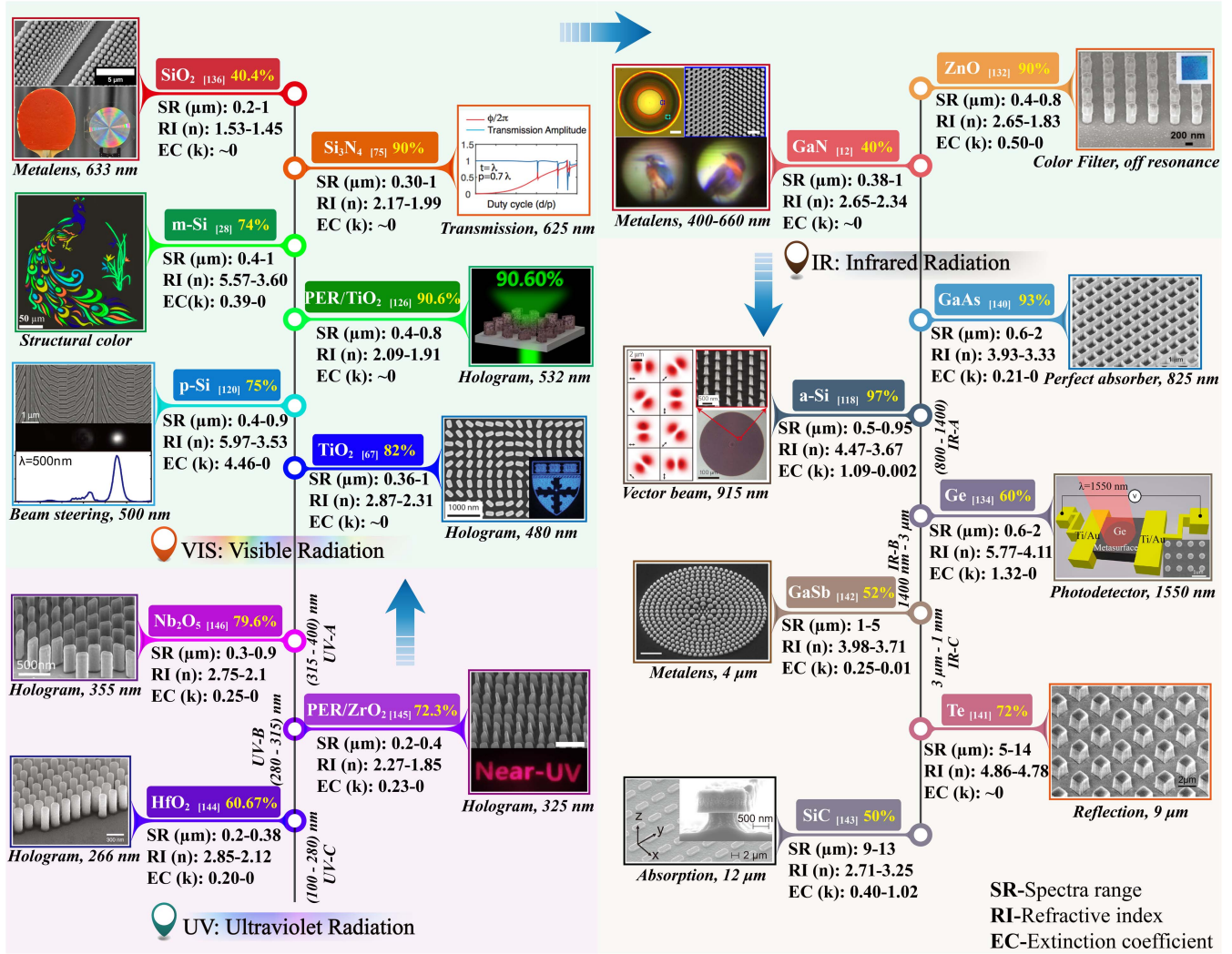


**Fig. 1** The schematic diagram illustrates typical fabrication techniques for all-dielectric metasurfaces, categorized into four parts. The first part encompasses standard nanolithography (NL) methods, specifically EBL, FIB lithography, and laser-based nanomanufacturing. The second part shows advanced NL techniques, such as grayscale lithography and scanning probe lithography (SPL). The third part explores mass manufacturing technologies, including molecular self-assembly, DUV lithography, and nanoimprint lithography. The fourth part delves into extreme nanofabrication techniques tailored for high-aspect-ratio, slanted, flexible, and multilayer nanostructures, respectively.

progress in cutting-edge nanofabrication technologies for all-dielectric metasurfaces, covering standard nanolithography techniques to extreme nanofabrication. In Secs. 1 and 2, we present diagrams illustrating the fabrication and properties of dielectric materials (Figs. 1 and 2). Moving to Sec. 3 we delve into the standard nanolithography method specific to dielectric materials and additionally introduce advanced and scalable nanofabrication techniques. Section 4 lists recent advancements in extreme fabrication techniques, leading to novel physical discoveries and applications. Finally, the article concludes in Sec. 5 with a perspective on the challenges and future directions in nanofabrication techniques for dielectric metasurfaces.

## 2 Resonant in All-Dielectric Metasurface

Commencing from Mie scattering as a starting point, ongoing research into the prevalent scattering phenomena in artificial micro-nanostructures has been actively advancing. The physics of light scattered by sub-wavelength particles typically involves Rayleigh scattering or surface plasmon interactions. These interactions are solely contingent on the properties of the material and not on the particle’s shape. In contrast, Mie resonance, also known as morphology-related resonance, depends on the particle’s size and geometric shape<sup>[110]</sup>. Particularly for dielectric nanodisks, incident light induces a substantial electromagnetic response. Specifically, the incident light couples with the circular displacement current of the electric field, leading to strong



**Fig. 2** Comparison of the optical properties of different dielectric materials and their high-efficiency applications across the UV to IR spectrum.

magnetic dipole (MD) resonance. Additionally, the wavelength of incident light must be comparable to the spatial dimensions of the particle, i.e.,  $2R \approx \lambda_{MD}/n$ , where  $n$  is the refractive index of the nanomaterial,  $R$  is the radius of the nanodisk, and  $\lambda$  is the wavelength of resonance. Hence, materials exhibiting a high refractive index possess the potential to reduce the dimensions of optical devices. The realization of MD resonance within the visible spectrum can be achieved by employing high-refractive-index nanoparticles with dimensions ranging from 100 to 200 nm. Electric dipole and MD moments are excited along the corresponding components of the incident field and are mutually perpendicular.

To accurately describe and thoroughly explain the phenomenon of uniform particle scattering equivalent to the wavelength, the Mie solution is employed to analyze the scattering field in the near-field and far-field regions, as well as the total scattering cross-section ( $C_{sca}$ ).  $C_{sca}$  can be expanded into an infinite series of spherical harmonics (Mie expansion):

$$C_{sca} = \frac{2\pi}{k^2} \sum_m^{\infty} (2m+1)(|a_m|^2 + |b_m|^2),$$

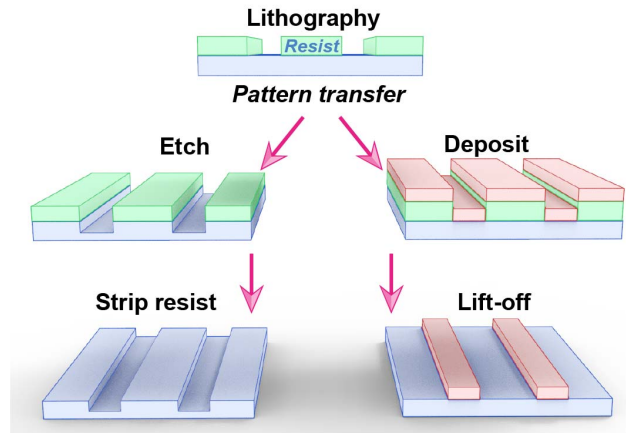
where  $a_m$  and  $b_m$  are the Mie coefficients for the  $m$ th order electric and magnetic multipole modes, respectively, and  $k$  is the wave number. Electric dipole (ED,  $a_1^e$ ), magnetic dipole (MD,  $b_1^m$ ), electric quadrupole (EQ,  $a_2^e$ ), and magnetic quadrupole (MQ,  $b_2^m$ ) constitute the first four terms in the Mie expansion.

Through the engineering of mode coupling in multiple dipole modes, the spectral overlap of various Mie modes can be harnessed to manipulate electromagnetic waves, enhance light-matter interaction, and control the directionality of the scattered field at a subwavelength scale<sup>[105]</sup>. This includes both single and arrays of dielectric structures, as well as hybrid dielectric-metallic systems that can support Mie resonance-based leak modes (also known as quasi-normal modes)<sup>[111,112]</sup>, waveguide modes<sup>[113-115]</sup>, and lattice modes<sup>[116,117]</sup>, thereby governing the optical properties of the metasystem. These types of resonances directly depend on the refractive index. Therefore, it is crucial to select the right material with suitable optical properties at desired wavelength ranges. The optical properties of the constituent materials are characterized by the complex refractive index as  $\tilde{n} = n + ik$ . The high refractive index ensures the strong confinement of incident light in the nano resonators and a full

manipulation of the wavefront. The extensive library of dielectrics, such as silicon (Si), gallium arsenide (GaAs), gallium nitride (GaN), and titanium dioxide ( $\text{TiO}_2$ ), among others, empowers researchers and scientists to design high-performance metapoptics (also known as meta-optics). These devices feature well-fabricated nanostructures capable of operating across a wide range of wavelengths, from ultraviolet (UV) to infrared (IR), thereby demonstrating attractive mechanisms and promising applications, as illustrated in Fig. 2. In the visible to near-IR (NIR) spectra, silicon [amorphous silicon (a-Si), monocrystalline (m-Si), and polycrystalline (p-Si)] has emerged as the predominant dielectric material. Its popularity stems not only from extensive use in the semiconductor industry but also due to its mature fabrication process and compatibility with modern CMOS technologies, aligning seamlessly with mass-manufacturability requirements. Silicon-based optical devices exhibit exceptional stability, leveraging their high refractive index and low losses in the visible range, near-zero losses in the IR region, and widespread application in meta-device design<sup>[73,118–120]</sup>. Despite efficiency challenges, particularly in the short wavelength range (around 250–450 nm), sophisticated nanostructure designs, such as adding a refractive index matching layer, offer the potential to mitigate losses and enhance performance<sup>[28,121]</sup>.  $\text{TiO}_2$  stands out as another promising material, displaying intriguing characteristics of a high refractive index and negligible optical losses in the visible to NIR range. It has found extensive use in high-efficiency all-dielectric metasurface design<sup>[67,101,122–125]</sup>.  $\text{TiO}_2$  is a CMOS-compatible material capable of addressing the absorption limitations of silicon, essential for cost-effective nanofabrication. Recent developments, such as synthesizing UV-curable polymer resist (PER) and  $\text{TiO}_2$  nanoparticles, have resulted in a nanoparticle-embedded resist exhibiting optical properties similar to pure  $\text{TiO}_2$ . Combined with nanoimprinting lithography, metasurfaces can be rapidly replicated from a master mold directly onto a substrate<sup>[126–128]</sup>. Other materials, including silicon nitride ( $\text{Si}_3\text{N}_4$ )<sup>[75,129]</sup>, GaN<sup>[11,2,130,131]</sup>, zinc oxide ( $\text{ZnO}$ )<sup>[132,133]</sup>, germanium (Ge)<sup>[134,135]</sup>, silicon dioxide ( $\text{SiO}_2$ )<sup>[136–138]</sup>, and GaAs<sup>[139–140]</sup>, are also attractive options widely used in visible to NIR applications. For middle-IR applications, narrowband semiconductors like tellurium (Te)<sup>[141]</sup>, gallium antimonide (GaSb)<sup>[142]</sup>, and polar crystals such as silicon carbide (SiC)<sup>[143]</sup> have already been implemented for all-dielectric meta-devices operating with Mie resonances. Furthermore, the UV range presents an intriguing field in nanophotonics, with the development of novel materials such as hafnium oxide ( $\text{HfO}_2$ )<sup>[144]</sup>, zirconium dioxide ( $\text{ZrO}_2$ )<sup>[145]</sup>, and niobium pentoxide ( $\text{Nb}_2\text{O}_5$ )<sup>[146]</sup>. These materials enable metasurfaces to contribute to the UV region, demonstrating high efficiency, ranging from 60% to 80%, as demonstrated by metahologram generation. Therefore, to achieve the intended optical function, it is crucial to select a dielectric material with suitable optical parameters in the UV to IR region and an available production method.

### 3 Nanofabrication Techniques of All-Dielectric Metasurfaces

The creation of metasurfaces is primarily grounded in lithography, a process that can be divided into two cases, as depicted in Fig. 3. The predetermined pattern of metasurfaces is initially transferred to a resist (photosensitive polymer) through exposure to specific energy radiation, such as photons, electrons,



**Fig. 3** The illustration clarifies the pattern transfer through the lithography process. Following the exposure of the resist film to a specific radiation source (such as photons, electrons, or ions), the intended pattern is transferred to the resist film through the developer. Subsequently, the pattern is transposed onto the dielectric materials, encompassing two cases. In the first case, the pattern is transferred through etching techniques; after the etching process, the final step involves stripping the resist through chemical or physical methods. In the second case, the dielectric material is deposited into the exposed and developed areas, followed by the lift-off process.

or ions, followed by the development process. Subsequently, the organized resist pattern serves as either the hard mask or the inverse pattern for the subsequent etching (left process flow in Fig. 3) or deposition processes (right process flow in Fig. 3). Depending on the dielectric material and specific application, various lithography techniques can be employed in the fabrication process. In this section, we provide a comprehensive summary of recent key lithography nanofabrication techniques for the fabrication of all-dielectric metasurfaces. This section will categorize these techniques into three types: standard nanolithography, advanced nanolithography, and large-scale nanolithography. While each technology comes with its inherent advantages and disadvantages, the overarching goal is to attain superior results, focusing on aspects such as high resolution, high speed, cost-effectiveness, and mass manufacture.

#### 3.1 Standard nanolithography techniques

The standard nanolithography process employed by the semiconductor industry incorporates EBL and FIB lithography<sup>[147,148]</sup>. These methods have found extensive use in dielectric fabrication, as illustrated in Figs. 1 and 2, marking a significant milestone in its evolution. In response to the challenges of cost-prohibitive and time-consuming processes over large scales, laser lithography has undergone substantial development. Table 1 presents a comparative analysis of these nanofabrication technologies.

##### 3.1.1 Electron beam lithography

EBL, a pivotal nanofabrication technique, precisely sculpts the electron beam resist at the nanoscale based on a computer-generated pattern. Acknowledged for its remarkable properties—high resolution, extensive design flexibility, and reliable

**Table 1** The Comparison of Three Main Standard Nanolithography Technologies

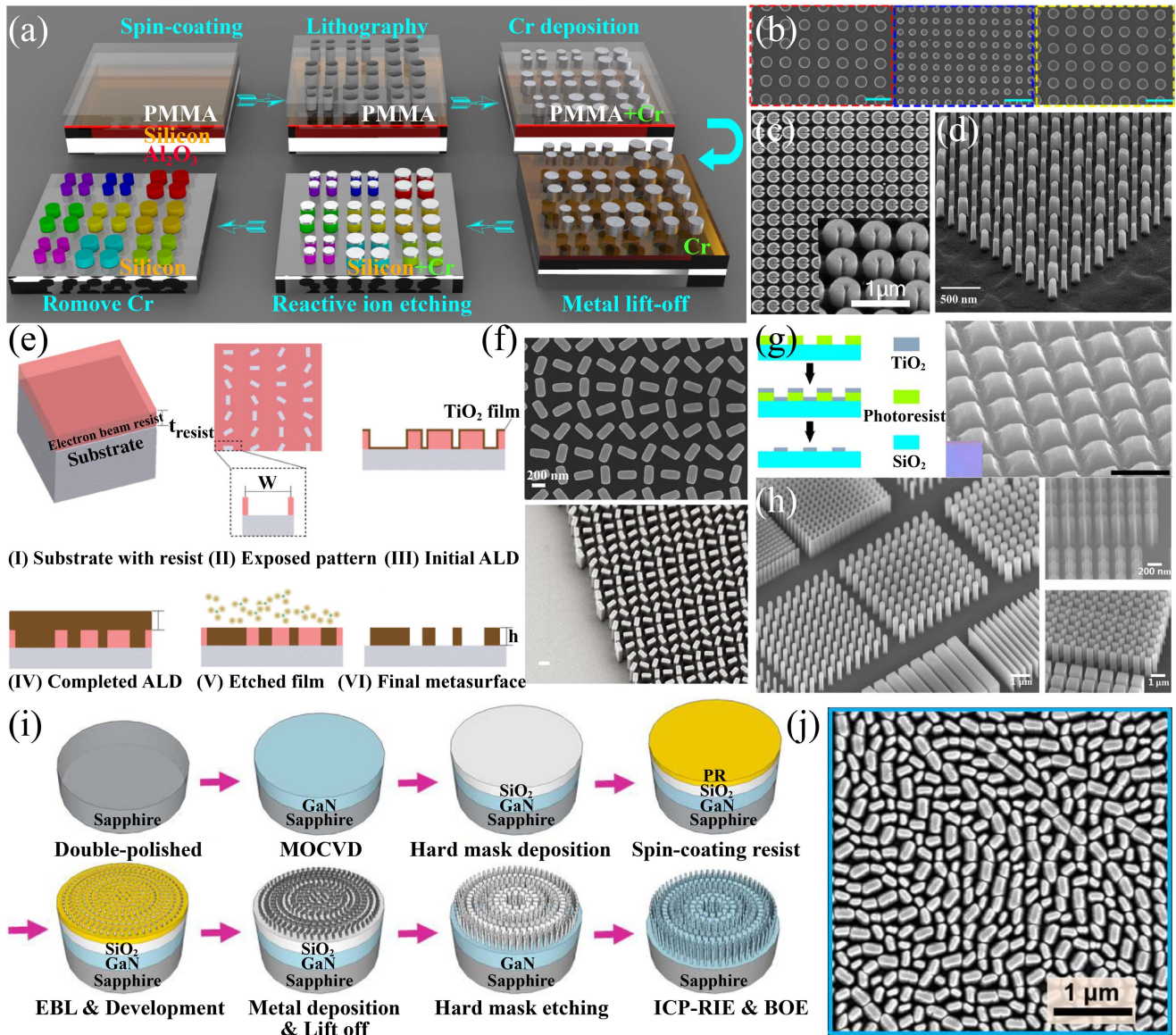
Feature	Electron Beam Lithography	Focused Ion Beam Lithography	Laser Lithography
Resolution	Extremely High (Sub-nanometer)	High (Nanometer to Sub-nanometer)	High (Nanometer to Micrometer)
Speed	Slow	Slow	Moderate to high
Material Selectivity	Good Material Selectivity	Good Material Selectivity	Material Dependent
Throughput (wafer-scale)	Limited	Limited	Limited-Moderate
Cost	High	Moderate	Moderate to low
Complexity of Setup	Complex	Moderate	Moderate
Dimension/Shape Design	Ideal for Complex Geometries and Small Features	Suitable for Planar Structures	Suitable for Complex 3D Structures

repeatability—EBL has emerged as the predominant technique for dielectric fabrication in recent decades<sup>[149]</sup>. Commercial EBL systems, typically operating within the 30 to 100 KeV energy range, are well-suited for fabricating sub-wavelength nanoantennas. The transfer techniques following exposure and development vary based on the specific dielectric material design and application. They are categorized into the bottom-up or top-down approach, with corresponding etching or lift-off processes. These approaches are grounded in film deposition methods, encompassing electron beam (E-beam) evaporation, sputtering deposition, chemical vapor deposition, and atomic layer deposition (ALD). The fundamental EBL process commences with the spin coating of a radiation-sensitive resist onto a designated substrate. The pattern is then delineated into the resist through high-energy electron radiation. In positive-type resist, the exposed area becomes soluble during development, while in negative-type resist, a cross-linking reaction in the exposed region renders it insoluble, and the background dissolves away. In the bottom-up approach, film deposition precedes lift-off to construct desired structures. Conversely, the top-down approach initiates with film deposition to achieve the dielectric layer at the desired thickness on the substrate before engaging in the EBL process, culminating the entire procedure with etching.

In the standard EBL fabrication procedure depicted in Fig. 4(a), monocrystalline silicon metasurfaces are crafted using the EBL technique, followed by a metal lift-off and dielectric etching process. Among these two approaches, the lift-off method utilizes a hard mask constructed with Cr, serving as a protector for subsequent etching. Yang *et al.* conducted experiments on commercially available silicon on the sapphire wafer, fabricating nanodisks with varying parameters of diameter/period arrays, showcasing high-performance structural color<sup>[28]</sup>. This approach exhibits advantages in terms of a broad gamut, high saturation, brightness, and resolution. The high-resolution scanning electron microscope (SEM) images in Fig. 4(b) reveal a roughness smaller than 10 nm. Due to its precise control over geometric precision, EBL stands out as the most widely used process for defining functional nanostructures with unique shapes. As depicted in Fig. 4(c), Gao *et al.* proposed C-shaped Si nanoantennas, demonstrating high-efficiency cyan and blue third-harmonic generation (THG) holograms<sup>[22]</sup>. This efficiency is achieved by introducing abrupt phase changes from 0 to  $2\pi$  to the C-elements, precisely controlled by the outer radius, inner radius, and opening angle. The mature and highly reproducible

CMOS-compatible fabrication technology of silicon allows for the efficient production of nanostructures, both horizontally and vertically. Figure 4(d) illustrates the smooth sidewall of silicon rectangle pillars, with an angle of nearly  $90^\circ$  in the vertical direction after reactive ion etching (RIE)<sup>[150]</sup>. This showcases the successful demonstration of a tunable full-color vectorial hologram with metamolecules.

However, due to the persistent ohmic losses in silicon materials<sup>[152–154]</sup>, especially at shorter wavelengths,  $\text{TiO}_2$  has emerged as a promising alternative in recent years and holds the potential to replace silicon in the field of dielectric metasurfaces<sup>[155–157]</sup>.  $\text{TiO}_2$  boasts a transparency window in the visible spectrum and a sufficiently high refractive index (2.87–2.31) for robust light–matter interactions. In the initial stages, Capasso’s group demonstrated high-efficiency  $\text{TiO}_2$  metasurfaces in the form of holograms for red, green, and blue wavelengths. The fabrication procedure, as illustrated in Fig. 4(e), involves using EBL to form reverse nanofins in resist, filling the exposed region with amorphous  $\text{TiO}_2$  through ALD, and then subjecting it to blanket RIE<sup>[67]</sup>. The remaining electron beam resist is subsequently stripped, resulting in  $\text{TiO}_2$  nanofins with a roughness of less than 1 nm, as shown in the top image in Fig. 4(f). Additionally, they fabricated metalenses for the visible range [bottom image in Fig. 4(f)] with efficiencies as high as 86%<sup>[15]</sup>. The geometrical parameters of the metalenses, defined by the resist, lead to the attainment of nearly  $90^\circ$  vertical sidewalls. However, current  $\text{TiO}_2$  metasurface fabrication techniques heavily rely on the ALD method, which is relatively slow and restricts thickness to values below 600 nm. In recent years, high-quality  $\text{TiO}_2$  films have been successfully formed using E-beam evaporation. In comparison to ALD, E-beam evaporation has proven to be more cost- and time-efficient<sup>[158]</sup>. Xiao’s group showcased the application of  $\text{TiO}_2$  metasurfaces in dielectric full-color printing through EBL and a straightforward lift-off process, as illustrated in Fig. 4(g)<sup>[30]</sup>. The nanostructures were constructed using E-beam evaporation to fill  $\text{TiO}_2$  into the reverse model. After a successful transfer of the reversed nanostructures onto the  $\text{TiO}_2$  film,  $\text{TiO}_2$  metasurfaces were ultimately formed through the lift-off process. The roughness of the  $\text{TiO}_2$  film measured at 0.66 nm signifies the achievement of an ultra-smooth surface. However, the lift-off method typically struggles to maintain perfect geometry due to the proximity effect during EBL. The cross-section of each subunit appears as a trapezoid rather than a rectangle in the vertical direction, as seen in Fig. 4(g). To address this issue, the researchers developed RIE technology for



**Fig. 4** The utilization of EBL in the fabrication of all-dielectric metasurfaces. (a) The schematic depicts the fabrication process of silicon-based structural color utilizing EBL.<sup>[28]</sup> Initially, a PMMA resist film is spin-coated onto the silicon-coated sapphire substrate. Subsequently, the resist is exposed to an electron beam and developed to form PMMA nanostructures. The sample is then transferred to an E-beam evaporator and coated directly with Cr films. After a lift-off process involving immersion in acetone, the PMMA is removed, and the nanostructures are effectively transferred to Cr. The silicon is later etched away using reactive ion etching with CHF<sub>3</sub> and SF<sub>6</sub> gases. Finally, Si metasurfaces are obtained by immersing the sample in chromium etchant. (b) The top-view SEM image of silicon nanodisks for color printing. The diameters and periods of these three colors are 170/320, 90/200, and 160/300 nm, respectively. The scale bar is 500 nm.<sup>[28]</sup> (c) The top-view SEM image displays the C-shaped silicon nanoantenna from nonlinear holographic all-dielectric metasurfaces. The insets include zoomed-in images showing high-resolution side-view SEM images.<sup>[22]</sup> (d) The SEM image with a tilted view of 45° presents the fabricated silicon rectangle pillars used for tunable full-color vectorial meta-holography.<sup>[150]</sup> (e) Fabrication process of high-efficiency TiO<sub>2</sub> metasurfaces<sup>[67]</sup>: (I) Application of the resist on fused silica. (II) Recording of the reversed metasurface pattern into the resist using EBL. (III) Deposition of TiO<sub>2</sub> through atomic layer deposition (ALD) into the exposed substrate. (IV) Formation of the final thick TiO<sub>2</sub> film, exceeding half the width of the maximum feature size after ALD. (V) Removal of residual TiO<sub>2</sub> and resist via reactive ion etching with a mixture of Cl<sub>2</sub> and BCl<sub>3</sub> ions. (VI) Completion of the final metasurface after eliminating remaining resist. (f) The top and side-view SEM image of fabricated ALD-based TiO<sub>2</sub> metalenses at visible wavelengths (scale

bar, 300 nm)<sup>[15]</sup>. (g) The fabrication process (left) of the TiO<sub>2</sub> metasurface with lift-off technology<sup>[30]</sup>. Top: Patterning of the photoresist with EBL. Middle: Formation of TiO<sub>2</sub> film through electron-beam evaporation technology. Bottom: Elimination of the photoresist through lift-off. Corresponding SEM image (right) of one metasurface (scale bar, 500 nm). (h) Tilt view SEM images of the etched TiO<sub>2</sub> nanostructures, including circular pillars, crosses, rectangle pillars, crosses, and rectangle pillars.<sup>[101]</sup> (i) Fabrication flow of the GaN-based structures for a broadband achromatic metalens in the visible spectra.<sup>[12]</sup> (j) SEM image of the on-axis focusing GaN-based metalens, applied in CMOS image sensor.<sup>[151]</sup>

the TiO<sub>2</sub> film fabricated by E-beam evaporation. Figure 4(h) demonstrates that the nanopillars exhibit nearly perfect vertical sidewalls, with a measured tilt angle of sidewalls around 89°–90°, effectively replacing the lift-off method<sup>[101]</sup>. Apart from TiO<sub>2</sub>, GaN stands out as another promising dielectric material with a high refractive index and negligible loss in the visible range<sup>[159]</sup>. It can be deposited using chemical vapor deposition. Tsai's group has pioneered a top-down approach to creating a GaN metalens, as depicted in Fig. 4(i)<sup>[12]</sup>. Employing integrated resonance unit elements within a metasurface framework, the successful demonstration of a flawless achromatic lens has been achieved<sup>[12]</sup>. The fabrication process initiates with EBL, followed by an etching process to shape the nanostructures. Figure 4(j) provides a top-view SEM image of a GaN metalens after the etching process<sup>[151]</sup>. While EBL possesses extraordinary advantages and has demonstrated numerous new concepts in metaphotonics, it is worth noting that it still grapples with disadvantages. These include limited area coverage, high expenses, and a complicated, time-consuming process. Additionally, there are costs associated with the purchase and maintenance of the EBL system.

### 3.1.2 Focused ion beam lithography

FIB lithography stands as a potent and versatile instrument based on the intricate interactions between ions and solids. With capabilities extending beyond mere nanofabrication, FIB integrates functions for both fabrication and characterization. The ion beam, typically derived from a liquid metal tip such as gallium (Ga), replaces electrons to directly pattern features onto the target film without the requirement of a photo-mask<sup>[160,161]</sup>. Through selective material removal or deposition, FIB enables the precise fabrication of planar micro/nano or three-dimensional (3D) structures, employing either the bottom-up or top-down approach<sup>[162–164]</sup>. These effects emerge from the dynamic exchanges of energy and momentum between the incident ions and target atoms. FIB techniques are analogous to SEM, allowing for simultaneous milling and observation of the surface in the work area, a configuration known as the dual-beam system. It has become one of the most advanced fabrication platforms for dielectric metasurfaces.

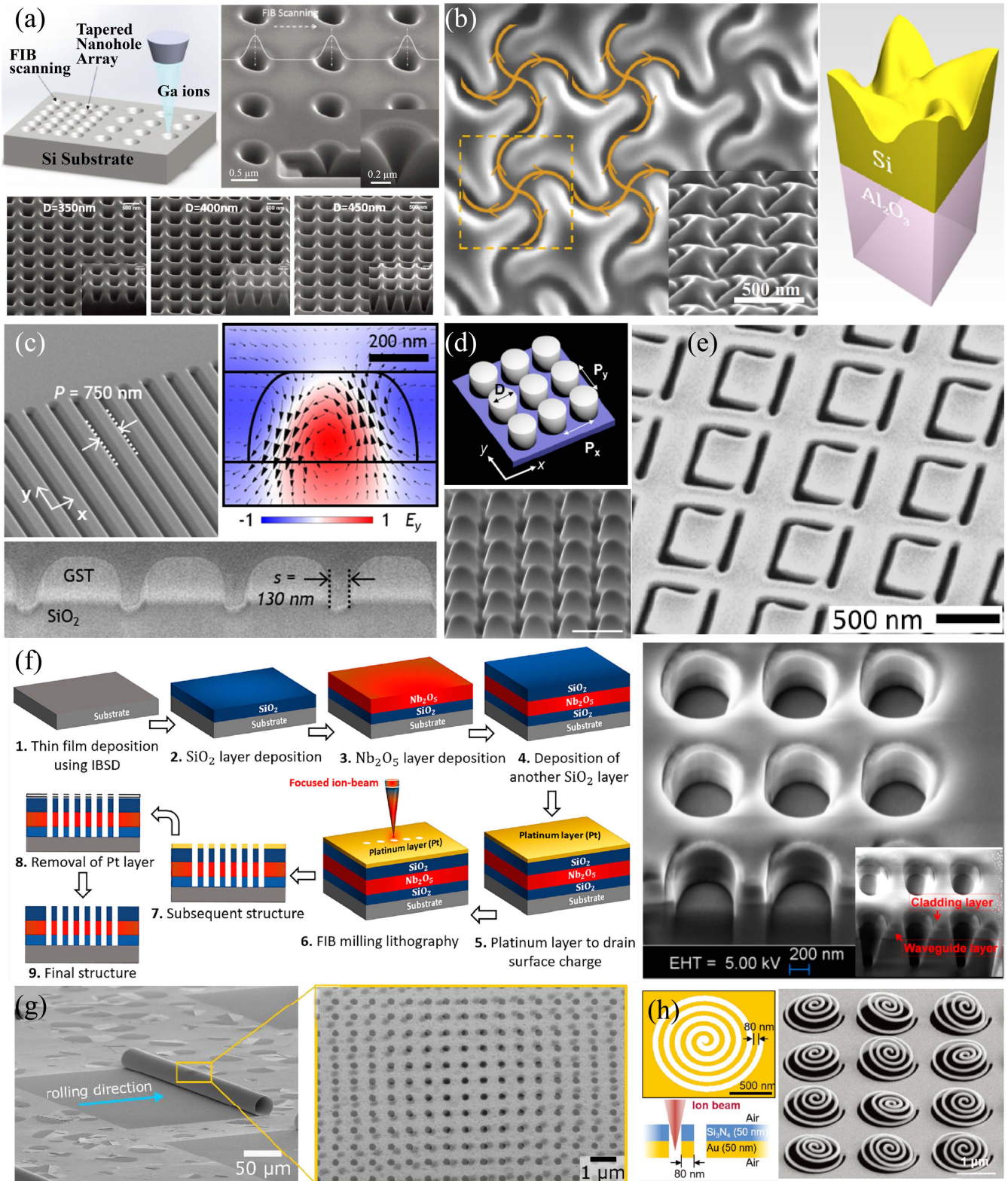
Garg *et al.* utilized FIB for direct milling of nanoholes on a silicon substrate, showcasing a wide color palette achieved through variations in nanohole diameter. Due to the Gaussian profile of the Ga ion beam, the nanoholes exhibited a tapered morphology, as depicted in the inset and bottom SEM images in Fig. 5(a)<sup>[165]</sup>. In comparison to alternative lithography techniques for color generation, the FIB process is notably simpler, requiring only a single step.

In-depth exploration into the sophisticated 3D structures using FIB etching has been undertaken by Gorkunov *et al.* The researchers engineered a chiral, 4-fold rotationally symmetric

monocrystalline silicon metasurface on sapphire through precise digital FIB patterning, as illustrated in Fig. 5(b)<sup>[166]</sup>. This meticulously crafted metasurface demonstrates exceptional transparency within the visible spectrum while concurrently manifesting robust optical chirality. Moreover, FIB technology finds application in the fabrication of all-dielectric phase-change materials. Illustrated in Fig. 5(c), a nanograting array is precisely milled by FIB onto the phase-change medium, germanium antimony telluride (GST)<sup>[167]</sup>. This method attains high-quality resonance and optical switchable all-dielectric metasurfaces in the NIR spectrum. Due to the FIB's independence from material selectivity, it extends beyond the fabrication of dielectric materials from the visible to NIR region<sup>[173,174]</sup>, enabling the fabrication of materials in the UV region, such as ZnO, as demonstrated by FIB in Fig. 5(d) for the nonlinear optical generation of vacuum UV light at a wavelength of 197 nm<sup>[168]</sup>. Moreover, the FIB not only demonstrates proficiency in handling single-layer dielectric films but also exhibits the capability to etch multilayer structures, as depicted in Fig. 5(f)<sup>[170]</sup>. In this instance, an Nb<sub>2</sub>O<sub>5</sub> layer is interposed between two SiO<sub>2</sub> cladding layers to form a symmetric slab waveguide design. The creation of a high-quality structure is achieved through optimized FIB parameters, including the ion beam current, area dose, and a number of process loops, as illustrated in the SEM image on the right side of Fig. 5(f). However, deviations from optimal parameters result in increased surface roughness and deeper milling holes than desired, as evidenced in the inset of Fig. 5(f). It is noteworthy that FIB nanofabrication has emerged as a crucial and extensively employed technical tool in the production of 3D nanostructures and devices. Researchers have illustrated free-standing 3D bilayer structures fabricated through FIB, as depicted in the SEM images in Figs. 5(e), 5(g), and 5(h), respectively, showcasing the nano-cantilevers (polycrystalline silicon/indium tin oxide)<sup>[169]</sup>, self-rolled multilayer metasurfaces (SiO<sub>2</sub>/Au)<sup>[171]</sup>, and 3D Archimedean spiral chiral structures (Si<sub>3</sub>N<sub>4</sub>/Au)<sup>[172]</sup>, and they demonstrated the application of optical modulation. While FIB milling is the primary method for defining dielectric nanostructures, particularly the nanoholes in thick films, due to its maskless and resistless advantages, it comes with certain limitations. These include high cost, extremely low throughput, and poor geometric precision control caused by the redeposition effect during milling. Additionally, there are aspect ratio limitations and the potential for sample damage.

### 3.1.3 Laser-based nanomanufacturing

In comparison to EBL and FIB, laser-based nanomanufacturing distinguishes itself as a versatile and standard tool for mask-free fabrication techniques. It involves the utilization of lasers to selectively remove or modify material on a substrate, enabling the creation of intricate patterns or structures at the micro- or nanoscale. This process finds widespread application in the



**Fig. 5** The utilization of focused ion beam lithography in the fabrication of all-dielectric metasurfaces. (a) FIB gallium ion milling in a Gaussian profile for the direct fabrication of subwavelength nanoholes on silicon through scanning, enabling multicolor generation. The SEM image (right) displays the nanohole, with the corresponding enlarged cross-sectional view as an inset. SEM images (bottom) of color filter arrays, with the right corner in each image showing the cross-section SEM obtained by FIB cutting.<sup>[165]</sup> (b) Chiral visible light metasurface patterned in monocrystalline silicon on sapphire using FIB. The SEM image on the left includes FIB paths and directions (curved



arrows), and the inset is a tilted SEM image. The right image presents a 3D model of a unit cell.<sup>[166]</sup> (c) All-dielectric phase-change reconfigurable metasurface consisting of a period grating fabricated by FIB milling in an amorphous Ge<sub>2</sub>Sb<sub>2</sub>Te<sub>5</sub> (GST) film on silica. The SEM images depict tilt and cross-sectional views, shown on the left and bottom, respectively.<sup>[167]</sup> (d) Schematic of the ZnO metasurface (top) for coherent vacuum ultraviolet light generation. Titled SEM image (bottom) of the FIB milled metasurface with scale bar of 300 nm.<sup>[168]</sup> (e) The SEM image of a section of a silicon membrane nano-cantilever array from nanomechanically reconfigurable all-dielectric metasurfaces for sub-GHz optical modulation.<sup>[169]</sup> (f) The process involves the stepwise fabrication of a dielectric slab waveguide photonic crystal, as illustrated on the left. On the right, SEM images provide both a top view and a cross-sectional view of dielectric structures fabricated using FIB milling lithography.<sup>[170]</sup> (g) Side and top-view SEM images of self-rolled multilayer metasurfaces.<sup>[171]</sup> (h) The 3D Archimedean spiral chiral metasurface milled by the gallium FIB and tilted SEM (right) images of the freestanding Au/Si<sub>3</sub>N<sub>4</sub> bilayer film.<sup>[172]</sup>

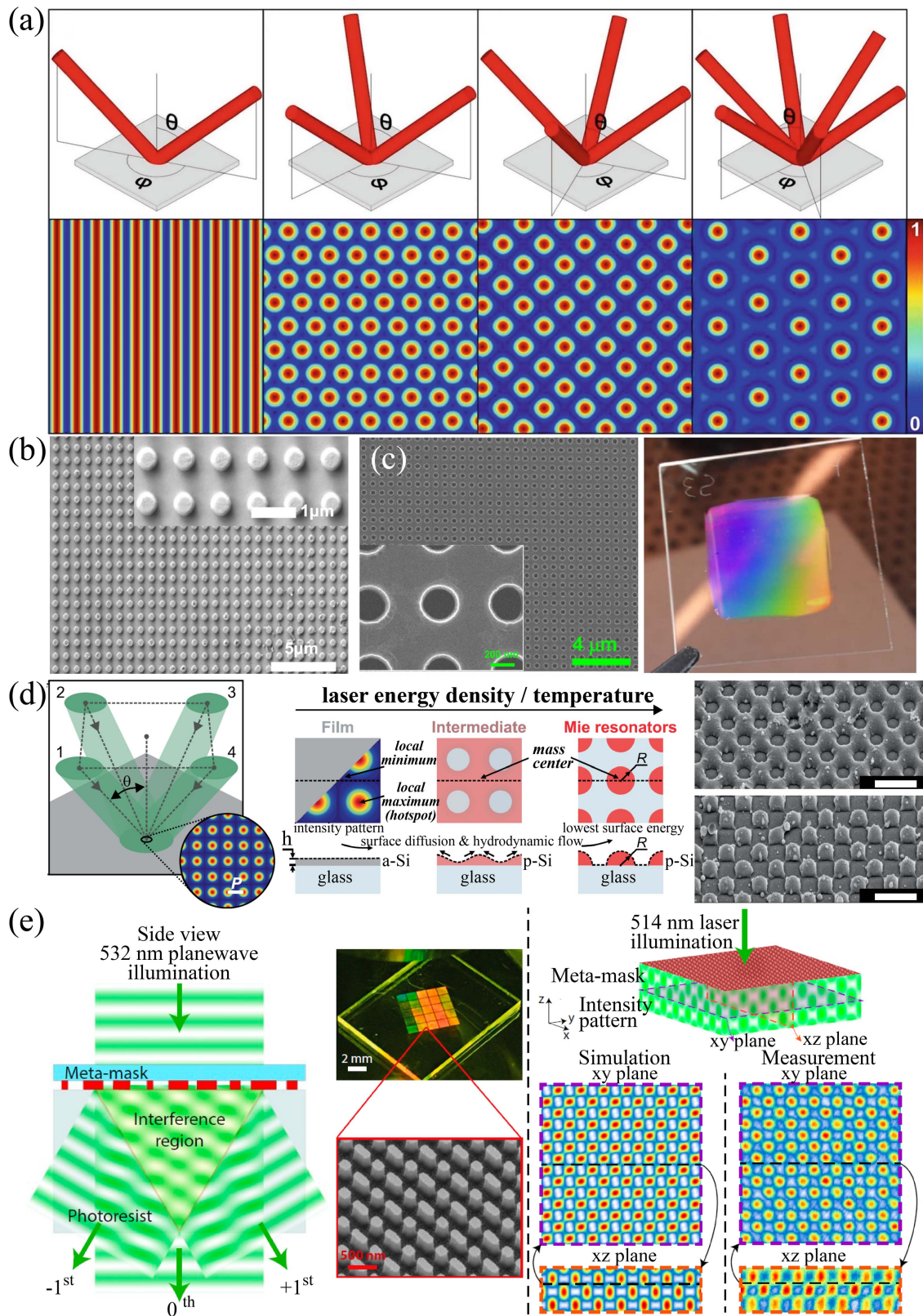
semiconductor industry for producing integrated circuits, micro-electromechanical systems, and various nanodevices. Laser-based nanomanufacturing stands out for its high precision, offering an avenue for high-efficiency, cost-effective, and large-scale fabrication. It facilitates the development of extremely small and detailed 3D optical features. Different variations of laser-based lithography exist, including techniques like laser interference lithography (LIL)<sup>[175,176]</sup> and laser direct writing (LDW)<sup>[138,177–182]</sup>, each with its specific advantages and applications. In this section, we delve into the recent advancements in LIL and LDW for dielectric metasurface fabrication. We explore the capabilities of LDW technology, encompassing laser-induced techniques<sup>[137,183,184]</sup>, laser melting or ablating<sup>[185–188]</sup>, and two-photon LDW<sup>[189–197]</sup>.

LIL is emerging as an innovative nano-patterning method without using a mask. Its prominence stems from its higher efficiency compared to EBL or FIB technology, coupled with a wide workspace and low cost. In LIL, two or more coherent light beams incident from different directions overlap on the photoresist layer, creating interference patterns. The control of these patterns is achieved through parameters such as the exposure dosage, development time, light wavelength, and angle of incident beams. Furthermore, laser interference beams with high-power pulses can directly process the material surface, relying on photothermal or photochemical mechanisms. In Fig. 6(a), the interference patterns of light intensity distribution with 2, 3, 4, and 6 overlapping beams are revealed<sup>[198]</sup>. The simplest scenario involves the interference of two beams, where beams with the same phase and polarization direction are symmetrical, sharing the same angle ( $\theta$ ) and azimuthal angles ( $\varphi$ ). In other instances, the overlap of 3 and 6 beams results in periodic structures arranged in a triangular lattice but with different periods. Particularly noteworthy is the interference pattern generated by the overlap of 4 beams, yielding a square array of intensity maxima. Leveraging this technology, researchers have devised various methods for constructing dielectric nanostructures. Chen *et al.* reported the fabrication of a 2D diffraction HfO<sub>2</sub> grating designed for achieving polarization-independent high-efficiency properties. This structure was created through twice orthogonal exposures by LIL, where two coherent laser beams overlapped in a negative-type photoresist. Subsequent to this, ion-beam etching technology was employed, as depicted in the SEM image in Fig. 6(b)<sup>[199]</sup>. Using the same procedures at positive photoresist, upon removal of the unexposed material, the pattern will change geometry from a nanodisk to a nanohole. Seo *et al.* introduced a large-area printed silicon hole array fabricated through LIL on a silicon-on-insulator (SOI) wafer

designed for broadband reflectors. The corresponding SEM images are depicted in Fig. 6(c)<sup>[200]</sup>. In addition to transforming the physical structure into the photoresist using the spatial intensity distribution of the interfering beams, Berzinš *et al.* demonstrated the application of single-pulse LIL with a four-beam picosecond laser, featuring a pulse duration of 300 ps and a wavelength of 532 nm [refer to the left image of Fig. 6(d)]. This technique was employed for the direct patterning of an amorphous silicon film into an array of Mie resonators, each a few hundred nanometers in diameter. As illustrated in the middle image of Fig. 6(d)<sup>[93]</sup>, the amorphous silicon film melted when exposed to sufficiently high temperatures, causing the continuous film to break into p-Si-based nanohole structures [Fig. 6(d), right top], referred to as the intermediate regime. If the laser energy density is further increased, it results in the formation of an array of Mie-resonant nanoparticles [Fig. 6(d), right bottom]. Moreover, LIL has faced challenges due to complex setups in multibeam lithography, leading to issues such as sensitivity and limited control over the fabricated structure. To address these concerns, researchers have introduced metasurface-mask-assisted LIL for 3D nanofabrication [left image of Fig. 6(e)]. This approach offers significant flexibility in patterning various periodic structures<sup>[201]</sup>. The diamond metasurface mask [middle SEM image of Fig. 6(e)] generates different diffraction orders that interfere to achieve a specific desired intensity pattern, ultimately realizing 3D structures in the photoresist [right SEM image of Fig. 6(e)].

Indeed, the use of multiple beams in LIL enables the simultaneous exposure of a large area, making it a faster and more cost-effective method for maskless massively parallel patterning. However, it faces challenges in achieving wafer-scale pattern uniformity and the freedom of pattern geometry, making it less competitive than e-beam lithography in these aspects. The limitations of LIL may stem from the absence of suitable large-area mirrors or other interference optics. Additionally, LIL operates as a batch process with constraints on producing only periodic patterns<sup>[202–204]</sup>.

In contrast to the LIL technique, LDW employs a focused laser beam to directly pattern a substrate, making it well-suited for creating intricate, customized 2D or 3D functional structures. LDW, also known as direct printing and digital writing, operates as a serial process where the laser beam traverses the substrate, selectively exposing or modifying the material. Over the past decade, research efforts have intensified, leading to the development of new applications for dielectric metasurfaces using LDW. This technique utilizes laser melting or ablating, laser-induced methods, and two-photon LDW, providing a



**Fig. 6** The utilization of LIL in the fabrication of all-dielectric metasurfaces. (a) Schematic representation (top row) of the interference setup with two, three, four, and six overlapping beams, along with the corresponding light intensity patterns (bottom row)<sup>[198]</sup>. (b) Top-view SEM of polarization-independent two-dimensional diffraction metal-dielectric gratings.<sup>[199]</sup> (c) SEM image (left) of the silicon nanohole array pattern on SOI substrate and the appearance (right) of large-area printed broadband membrane reflectors by LIL. The inset shows the enlarged SEM image.<sup>[200]</sup>

(d) Mie-resonant silicon-based metasurfaces via single-pulse LIL: Schematic (left) illustrating the four-beam interference setup and the resulting intensity distribution. Mechanism of silicon transformation (middle) utilizing the interference intensity pattern. Top view (right) SEM images of nano-holes (intermediate state, a-Si) and nanoparticles (Mie resonator state, p-Si) patterned from the Si film, respectively. Scale bar = 1  $\mu\text{m}$ .<sup>[93]</sup> (e) Metasurface-generated complex 3D optical fields for LIL: Concept (left) of a metamask generating specific diffraction orders to create a desired 3D pattern in the photoresist. Optical image (middle) of the fabricated optimized diamond metamask. Simulated and measured cross-sections (right) of the captured 3D intensity pattern under 514 nm laser illumination.<sup>[201]</sup>

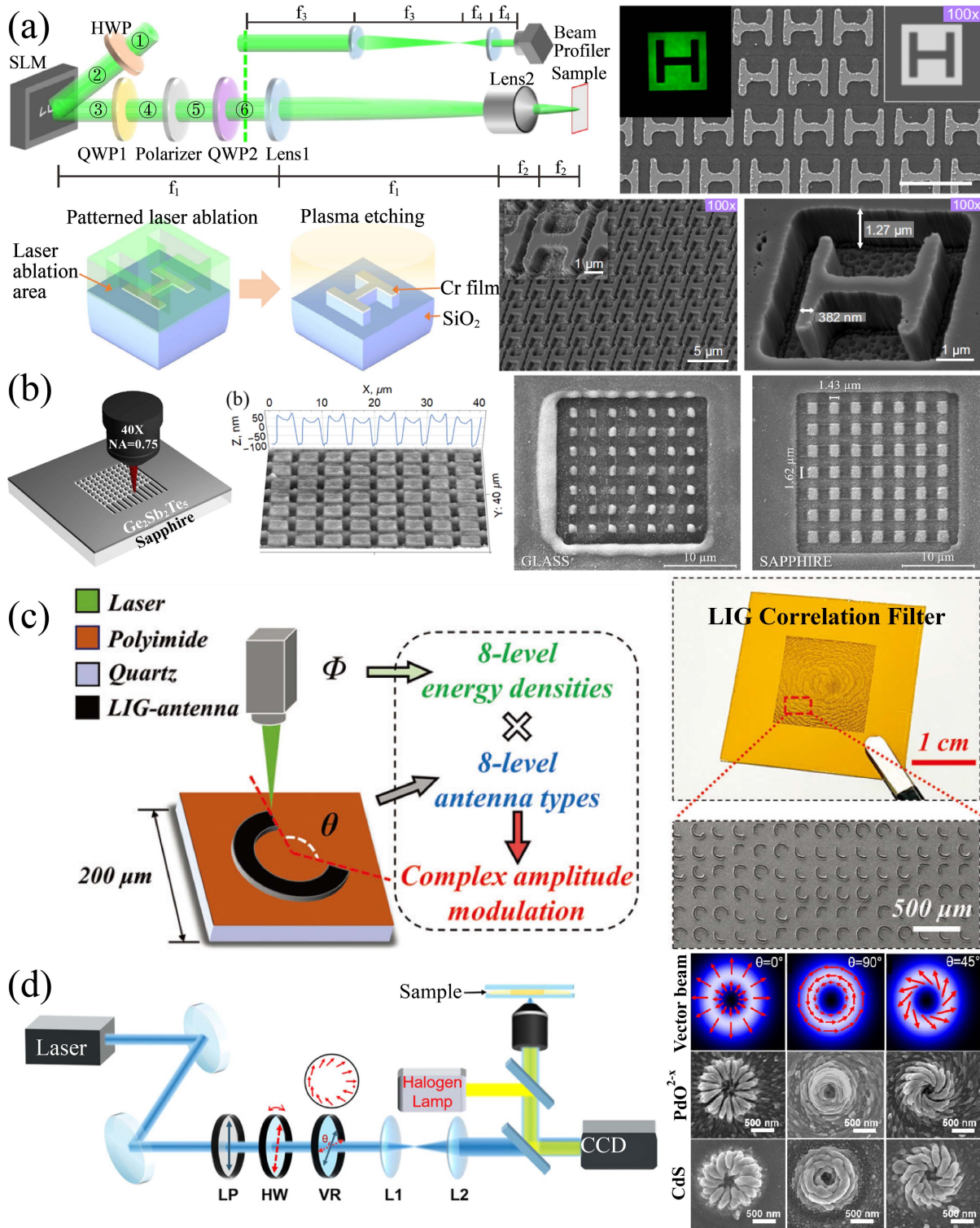
single-step, lithography-free, and cost-efficient approach for large-scale fabrication of both ordered and disordered structures. Notably, LDW eliminates the need for a clean room and vacuum chambers. Figure 7(a) illustrates the utilization of patterned pulse LDW lithography for the fabrication of metasurfaces comprising sub-wavelength structural units<sup>[205]</sup>. Employing the pulse LDW system depicted on the left image of Fig. 7(a), the film undergoes ablation and recasting, forming fine H-shaped units [top right SEM image of Fig. 7(a)] as a result of the scanning of the patterned femtosecond laser (520 nm) achieved by the phase mask on the spatial light modulator (SLM). Subsequently, H-shaped arrays for dielectric metasurfaces are produced, with the etching process utilizing the nanopatterned films as hard masks [bottom image of Fig. 7(a)]. Furthermore, Bocek *et al.* experimentally demonstrated the direct fabrication of the tunable metasurfaces from  $\text{Ge}_2\text{Sb}_2\text{Te}_5$  films by exploiting the LDW technique<sup>[206]</sup>. Figure 7(b) displays the SEM images of the metasurface fabricated by laser (780 nm) inscription on GST films deposited on glass and sapphire substrates. Another one-step LDW fabrication method is based on the laser-induced technique. Figure 7(c) illustrates the application of the laser-induced graphene technique for processing a complex amplitude-modulating metasurface device<sup>[207]</sup>. A 15 mm  $\times$  15 mm metasurface composed of C-shaped graphene antennas can be fabricated by one-step laser writing in 34 s [visible in the photograph and SEM image in Fig. 7(c) on the right]. More complex 3D structures can also be fabricated using the laser-induced method. Lu *et al.* introduced a polarization-directed chiral growth of dielectric materials ( $\text{PdO}_2 - x$ , CdS) induced by a laser with the vector beam<sup>[208]</sup>. As shown in the right image of Fig. 7(d), the 3D chiral unit of metasurfaces is induced in an inorganic precursor solution under continuous-wave laser irradiation with different vectors.

Another advanced LDW technology two-photon lithography, also referred to as two-photon polymerization, represents an advanced LDW technology for the fabrication of true 3D micro/nanostructures. This method exploits nonlinear photon absorption by photopolymers, enabling the construction of well-defined structures with sub-100-nm resolution. While standard photolithography is commonly employed for 2D structure fabrication, relying on one-photon absorption in a photosensitive medium, the two-photon process involves tightly focusing the beam of an ultra-fast laser inside the volume of a transparent material. This leads to the absorption of two photons, inducing local polymerization. In Fig. 8(a), the true 2D photonic structures or dielectric metasurfaces, along with their inverted counterparts featuring square  $C_{4v}$  and hexagonal  $C_{6v}$  lattice symmetry, are showcased. These structures are fabricated using two-photon LDW with femtosecond pulses at 780 nm. Experimental results demonstrate a transition from the multi-order diffraction regime characteristic of photonic crystals to

the regime where only zero-order diffraction is observed, serving as a distinctive feature of metasurfaces<sup>[209]</sup>. Zhan *et al.* have introduced a computational inverse design method and implemented the fabrication of arrays of spherical Mie scatterers with the aim of generating 3D optical field patterns through two-photon lithography. The SEM image in Fig. 8(b) illustrates the outcome of this process, showcasing the arrays of scatterers. Notably, these scatterers exhibit a deviation from perfect spherical shapes<sup>[210]</sup>. Following this, a more intricate inverse-designed structure intended for a free-form NIR polarization beam splitter is produced through 3D printing using two-photon lithography. The SEM image of the device is depicted in Fig. 8(c)<sup>[211]</sup>. The device was printed layer by layer vertically on top of the fused silica substrate. Furthermore, two-photon lithography offers an exceptional opportunity for the direct printing of optical functional metasurfaces on a fiber tip, as compared to techniques such as EBL<sup>[212]</sup> and FIB<sup>[213]</sup>. Plidschun *et al.* employed two-photon lithography to create nanopatterns on the end faces of modified single-mode optical fibers for multi-focus holography [Fig. 8(d)]<sup>[214]</sup>. Additionally, Hadibrata *et al.* achieved 3D printing of a metasurface consisting of a circular grating-like structure on an optical fiber tip for metalens applications [left SEM image of Fig. 8(f)]<sup>[215]</sup>. Simultaneously, they employed a computational inverse-design method based on an objective-first algorithm to design this structure. They showcased the application of the fabricated metalens with a fiber tip using a homemade two-photon polymerization process, as depicted in the right image of Fig. 8(f), where the pattern “NU” is produced using the LDW setup. In Fig. 8(e), perfect absorber metasurfaces with a unique 3D helix-based dielectric architecture for the IR spectral range are presented using the LDW<sup>[216]</sup>. The fabricated samples showcase resonant absorption bands with peak absorbance exceeding 80% in the wavelength range of 6–11  $\mu\text{m}$ . LDW is typically a slower process, involving the scanning of a focused laser beam point by point, especially in the context of 3D printing by two-photon lithography, making it unsuitable for wafer-scale fabrication. Nevertheless, despite these limitations, LDW continues to be a valuable technique in applications where high precision and resolution are paramount, such as in microelectronics, photonics, and biomedical devices.

### 3.2 Advanced nanolithography techniques

In recent years, in addition to the mentioned standard nanolithography techniques such as EBL, FIB, and laser-based lithography, innovative lithography methods have been well developed. These updated methods are based on the combination of EBL, FIB, and LDW, and they include grayscale lithography<sup>[217–220]</sup>, multistep lithography<sup>[221–225]</sup>, and the novel fabrication of scanning probe lithography (SPL) based on the atomic force microscope, which is also studied in dielectric metasurface



**Fig. 7** The utilization of LDW in the fabrication of all-dielectric metasurfaces. (a) The patterned pulse LDW approach for creating sub-wavelength features. The schematic illustration (top) shows the pulse LDW system, and the SEM image of fabricated H-shaped arrays with sub-wavelength feature size is visible. Scale bars = 5  $\mu\text{m}$ . Fabrication flow (bottom) of structures by etching after pulse LDW (Cr used as masks on fused silica substrate), along with SEM images of the H shapes.<sup>[205]</sup> (b) The fabrication process of  $\text{Ge}_2\text{Sb}_2\text{Te}_5$  metasurfaces by LDW. The schematic on the left illustrates the laser casting process of a GST film through LDW. The AFM image in the middle showcases the fabricated metasurface with a square array, while the SEM images on the right depict the metasurface on glass and sapphire substrates, respectively.<sup>[206]</sup> (c) THz

optical pattern recognition enabled by a complex amplitude modulating metasurface through LDW: Schematic (left) of the fabrication of laser-induced graphene C-shaped antennas by LDW with different orientations and angles. Optical and SEM image (right) of the fabricated metasurface.<sup>[207]</sup> (d) Polarization-directed growth of spiral nanostructures by LDW with vector beams: Illustration of optical setup (left). Simulated beam profile with different vectors and SEM images of the chiral patterns formed with different vector beams (right).<sup>[208]</sup>

fabrication<sup>[226,227]</sup>. These approaches provide avenues for designing and fabricating more intricate nanostructures.

### 3.2.1 Grayscale lithography

Grayscale lithography stands out among advanced lithography methods, offering a novel approach to designing and fabricating intricate nanostructures. In contrast to nanolithography techniques like EBL, FIB, and laser lithography, which primarily focus on the fabrication of nanostructures with the same height, grayscale lithography introduces an additional level of control by enabling variable light intensity during the exposure process. This innovation addresses the limitation of these nanofabrication methods that often neglect the ability to vary the height of nanostructures on a single metasurface. In grayscale lithography, the intensity of light can be finely tuned, providing a continuum of exposure levels rather than a binary on-off state that achieves arbitrary structure control in the  $Z$  direction and  $X$ - $Y$  direction simultaneously. This fine control empowers the creation of nanostructures with varying heights or depths, significantly enhancing the spatial regulation of nanostructures. This capability opens the door to the development of more sophisticated photonic devices with tunable height, including applications in color printing, multi/hyperspectral imaging, and metalenses<sup>[228–232]</sup>. EBL stands out as a compelling choice for grayscale nanopatterning. Geng *et al.* innovated a grayscale nanofabrication method using EBL and ALD, as illustrated in the schematic in Fig. 9(a)<sup>[233]</sup>. The process involves employing EBL to inscribe designed nanopatterns with varying irradiation doses, resulting in resist nanoholes with different depths that serve as a grayscale template. Subsequently, ALD is utilized to fill these nanoholes with  $\text{TiO}_2$ . Finally,  $\text{TiO}_2$  nanostructures with diverse heights are constructed after removing the resist and the underlying  $\text{TiO}_2$  film through etching. The SEM images depict a simultaneous structural change in three dimensions of the nanopillars along the  $X$ - $Y$ - $Z$  directions. Additionally, they proposed achieving structural color display through height modulation [as seen in the bottom image of Fig. 9(a)]. An alternative to EBL for grayscale nanolithography is FIB milling<sup>[234,235]</sup>. Hentschel *et al.* reported the fabrication of resonant Mie voids through FIB milling into silicon wafers. The top SEM image in Fig. 9(b) displays voids with varying diameters and depths. Furthermore, they explored applications in color printing using voids of constant diameter and varying depth, as depicted in the middle and bottom images of Fig. 9(b)<sup>[234]</sup>.

Two-photon LDW presents a novel and alternative approach for grayscale nanolithography. Wang *et al.* explored color generation from nanopillars with varying heights, periodicities, and diameters in dielectric material using two-photon grayscale lithography [as depicted in the left image of Fig. 9(c)]<sup>[236]</sup>. The SEM images of fabricated nanopillars with different heights are shown in the right image of Fig. 9(c). This method allows for the creation of arbitrary colorful and grayscale images by precisely controlling the parameters. In addition to these techniques, UV lithography can also be employed in grayscale

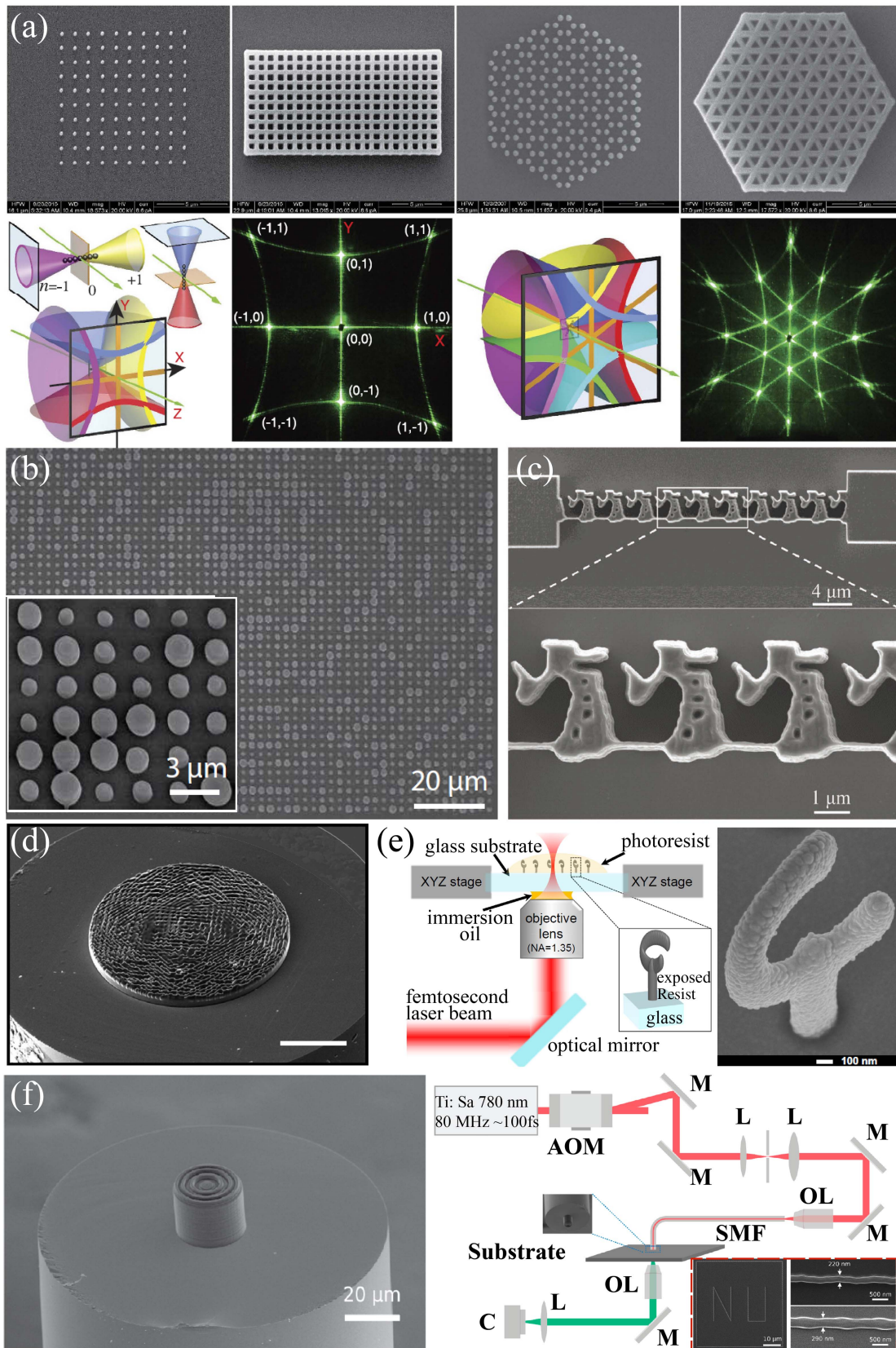
lithography. As illustrated in Fig. 9(d), to showcase multispectral filter arrays in a single lithographic step across the visible to NIR spectrum, researchers combined grayscale lithography with metal-insulator-metal Fabry-Perot cavities, where the exposure dose controls cavity thickness. Initially, low-volume EBL grayscale lithography was employed, followed by the demonstration of large-volume UV mask-based photolithography using the binary mask approach<sup>[237]</sup>.

### 3.2.2 Multistep lithography

Multistep lithography involves a sequence of lithographic processes aimed at creating intricate nanostructures. This strategy incorporates multiple patterning steps, each contributing to the final design. Multistep lithography is chosen when a single lithographic step proves insufficient for achieving the desired complexity. In Fig. 10(a), Lu *et al.* demonstrated the two-step lithography fabrication of a versatile silicon-based metasurface with superwettability for self-cleaning and dynamic color response<sup>[238]</sup>. This involved utilizing a Cr mask for the optical metasurface through EBL, followed by fabricating the resist mask for wettability-supporting structures using laser beam lithography. The process continued with plasma etching and mask removal, concluding with a treatment to achieve the desired wettability state. In another instance, researchers employed a combination of EBL and photolithography to fabricate a synthetic aperture metalens, as outlined in Fig. 10(b)<sup>[221]</sup>. This fabrication process included patterning a-Si nanopillar-based metalens and an aluminum (Al) mask to block unwanted light transmission outside the metalens aperture. The patterning steps were carried out through EBL and photolithography, respectively. Hybrid patterning techniques, combining various lithography methods, have been developed to realize metasurfaces with more complex nanostructures. Additionally, these techniques enable the creation of hybrid metasurfaces comprising both metal and dielectric materials with different geometries. In the left image of Fig. 10(c), Guo *et al.* investigated multipolar coupling in metal-dielectric metasurfaces, involving arrays of gold nanorods coupled to silicon nanodisks<sup>[222]</sup>. The fabrication process involved a two-step EBL process combined with a precision alignment step. The first step defined the silicon nanodisks through EBL and etching on an SOI wafer. In the second step, cross-shaped gold alignment marks were lithographically defined at the corners of the nanodisk arrays for fabricating plasmonic gold nanorods through EBL and lift-off. The resulting nanoantennas are depicted in the SEM image on the right side of Fig. 10(c).

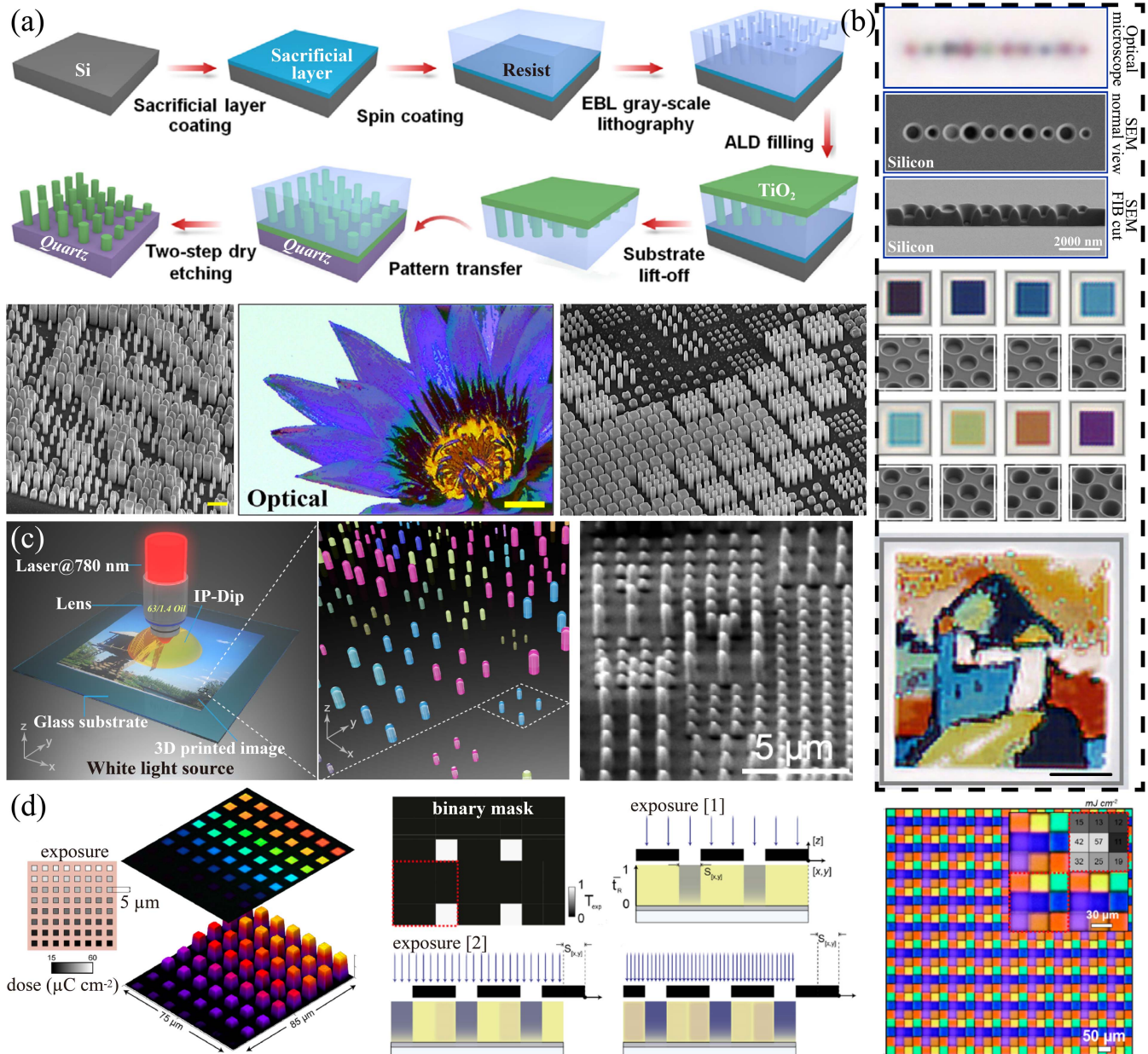
### 3.2.3 Scanning probe lithography

SPL is another direct-write nanolithography technique that has emerged as an area of research in the field of atomic force microscopy. SPL employs a probe or tip in close proximity to a sample to directly pattern nanometer-scale features on the sample surface. This method offers high precision and flexibility in the creation of nanostructures. The interaction between



**Fig. 8** The utilization of two-photon lithography in the fabrication of all-dielectric metasurfaces. (a) Dielectric metasurfaces in the optical diffraction with a fine structure: SEM images of fabricated dielectric particle/inverted counterpart structures and corresponding optical diffraction patterns.<sup>[209]</sup> (b) SEM images of the device for controlling 3D optical fields via inverse Mie scattering.<sup>[210]</sup> (c) SEM

images of an inverse-designed near-infrared polarization beam splitter fabricated by two-photon LDW.<sup>[211]</sup> (d) SEM image of a two-photon LDW-printed hologram on the single-mode fiber facet (scale bar is 20  $\mu\text{m}$ ).<sup>[214]</sup> (e) Realization of a helix-based perfect absorber for IR spectral range using two-photon LDW: Fabrication process (left) of dielectric templates. Zoom-in SEM image (right) of a single-turn metallic helix.<sup>[216]</sup> (f) Inverse design and 3D printing of a metalens on an optical fiber tip for two-photon LDW: SEM image (left) of the fabricated metalens on top of the fiber. Schematic of the homebuilt two-photon polymerization setup (right). The inset shows the SEM image of the pattern “NU” produced by the metalens using the homemade setup.<sup>[215]</sup>



**Fig. 9** The utilization of grayscale lithography in the fabrication of all-dielectric metasurfaces. (a) Grayscale lithography enables the creation of height-gradient-tunable nanostructure arrays, as illustrated in the schematic (top). This method utilizes grayscale EBL, ALD, and a transfer process for assembling nano-configurations with varying heights. The bottom row displays typical SEM images of  $\text{TiO}_2$  nanopillars with height variations, showcasing the structural color metasurface after implementing height regulation.<sup>[233]</sup> (b) FIB grayscale lithography facilitates color printing using Mie voids in a silicon substrate. The top part illustrates conically shaped voids with varying diameters and depths in a bulk silicon wafer. In the middle, optical microscope and SEM images

showcase selected sizes and depths of Mie voids. The bottom section presents an optical microscope image of the color-printed image.<sup>[234]</sup> (c) Two-photon grayscale LDW is employed for 3D-printed low-index nanopillars, as outlined in the schematic of the fabrication process (left). The right side features a tilted view SEM of the 3D printed colorful painting, revealing pillars with varying heights, diameters, and periodicities.<sup>[236]</sup> (d) Custom multispectral filter arrays are fabricated using EBL and laser grayscale lithography. The left section demonstrates a dose-modulated pixel array by EBL, displaying the dose-modulated pattern, optical micrograph, and corresponding AFM data. The middle part outlines the fabrication flow of photolithography-based multispectral filter arrays using a binary photomask. The right side presents an optical micrograph (transmission) of a different region of the wafer, with a labeled equivalent exposure pattern (inset).<sup>[237]</sup>

the probe and surface is pivotal in the patterning process, and SPL can be categorized into various modes based on this interaction, including thermal, electrical, mechanical, and diffusive processes during the patterning operation. Figure 11(a) illustrates the SPL operation, depicting the intricate interactions involved in the process<sup>[239]</sup>.

In thermal SPL, localized material modifications, such as crystallization, evaporation, and melting, are induced using thermal energy from a heated tip<sup>[242]</sup>. Lisunova *et al.* showcased high-aspect-ratio nanopatterning in silicon through combined thermal SPL and dry etching, as illustrated in Fig. 11(b)<sup>[240]</sup>. This method relies on the self-amplified depolymerization of a poly(phthalaldehyde) (PPA) thin film in contact with a heated probe. The SPL tool was utilized to inscribe patterns in the PPA thin film, which were then transferred into the SiO<sub>2</sub> hard mask through etching. In the final etching step, the SiO<sub>2</sub> pattern was further transferred into Si using deep RIE. The SEM images portraying the transferred patterns and the silicon sidewall are presented in the inset of Fig. 11(b), showcasing remarkable accuracy and uniformity, with a sidewall angle profile measuring approximately  $87^\circ \pm 2^\circ$ . SPL, grounded in a tip-induced electric field, emerges as a promising avenue for prototyping. Researchers have demonstrated a precise etching strategy for VO<sub>2</sub> by directly writing on a VO<sub>2</sub> film with a negative tip bias, followed by sonication removal of the written area. As depicted in the left image of Fig. 11(c), the results indicate that VO<sub>2</sub> can be etched layer by layer through alternately repeating tip modulation and sonication, allowing for the creation of arbitrary patterns<sup>[241]</sup>. Leveraging this approach, a metasurface was designed by arranging VO<sub>2</sub>-gold nanoblocks with varying sizes and heights, enabling spectrally selective tunable reflectivity in the near- and mid-IR, as shown in the right image of Fig. 11(c).

These innovative lithography techniques, encompassing grayscale lithography, multistep lithography, and probe scanning lithography, broaden the horizons of nanostructure design and fabrication. Researchers and engineers can select the most fitting method according to the particular demands of their applications, providing enhanced flexibility in crafting intricate nanostructures. Nonetheless, it is essential to note that all of these methods are characterized by low throughput and high costs. Substantial enhancements are imperative to enable large-area patterning for widespread application in large-scale manufacturing processes.

### 3.3 Large-scale nanolithography techniques

Despite the rapid advancements in nanofabrication technology, most of these techniques remain in the laboratory stage and are far from practical application and commercialization. This is primarily due to extended processing times and the absence

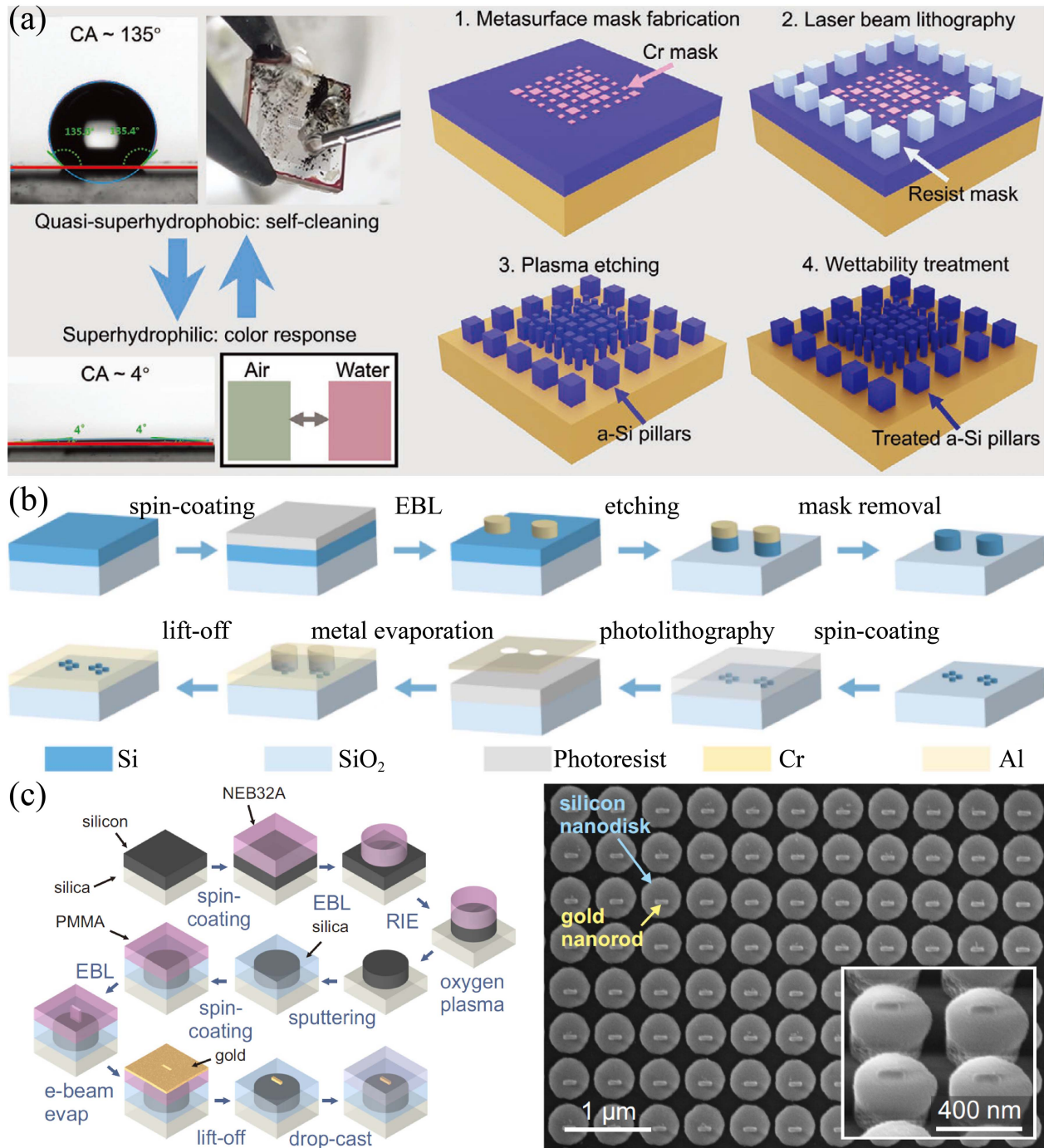
of mass production capabilities, which are crucial for semiconductor devices and integrated circuits. Industrial applications demand large-scale serial production of 2D microstructures, repeated tens of thousands of times, with silicon wafer surface areas ranging from 2 up to tens of inches. It is evidently imperative to develop large-area fabrication techniques with enhanced flexibility, high precision, and uniformity<sup>[94,243,244]</sup>. Several techniques have been proposed and studied to boost high-throughput productivity for larger-scale metasurface fabrication, incorporating methods such as UV lithography<sup>[245–251]</sup>, nanoimprinting lithography<sup>[97,245,252–258]</sup>, and self-assembly-based nanofabrication<sup>[259–263]</sup>. These approaches collectively contribute to the continuously evolving landscape of nanotechnology, paving the way for the cost-effective commercialization of metasurfaces.

#### 3.3.1 UV lithography

Wafer-scale UV lithography employs UV light sources with specific wavelengths to pattern semiconductor wafers during the fabrication of integrated circuits. In semiconductor lithography, two types of photolithography tools are utilized in transferring patterns onto silicon wafers during the manufacturing process: the “stepper system” and the “scanner system”<sup>[266]</sup>. The stepper employs a step-and-repeat process, where the reticle (mask) and wafer remain stationary for each exposure, exposing one small part of the wafer at a time. The pattern is replicated across the wafer by repeatedly exposing and incrementally stepping the wafer position. On the other hand, the scanner utilizes a scanning system where the reticle and wafer move synchronously during the exposure process, exposing the entire wafer simultaneously.

Light is systematically scanned across the reticle, and the resulting exposed pattern is projected onto the entire wafer. Utilizing these systems, three UV lithography techniques are currently employed in metasurface fabrication: i-line ( $\lambda = 365$  nm) stepper lithography, argon fluoride (ArF) DUV ( $\lambda = 193$  nm) immersion scanner lithography, and krypton fluoride (KrF) stepper DUV ( $\lambda = 248$  nm) lithography. The inherent planarity of metasurfaces aligns with fabrication processes directly compatible with the mature integrated circuit industry, providing opportunities for mass production. Employing i-line stepper lithography, as depicted in Fig. 12(a), researchers successfully fabricated a single 2-cm-diameter [refer to photograph in Fig. 12(a)] silicon-based NIR metalens, showcasing high-quality imaging and diffraction-limited focusing<sup>[267]</sup>. However, the large wavelength of i-line stepper lithography prevents the creation of nanostructures with smaller feature sizes suitable for working at visible wavelengths. Current state-of-the-art photolithography tools employ DUV light with wavelengths of 248 nm (KrF) and 193 nm (ArF). KrF DUV lithography

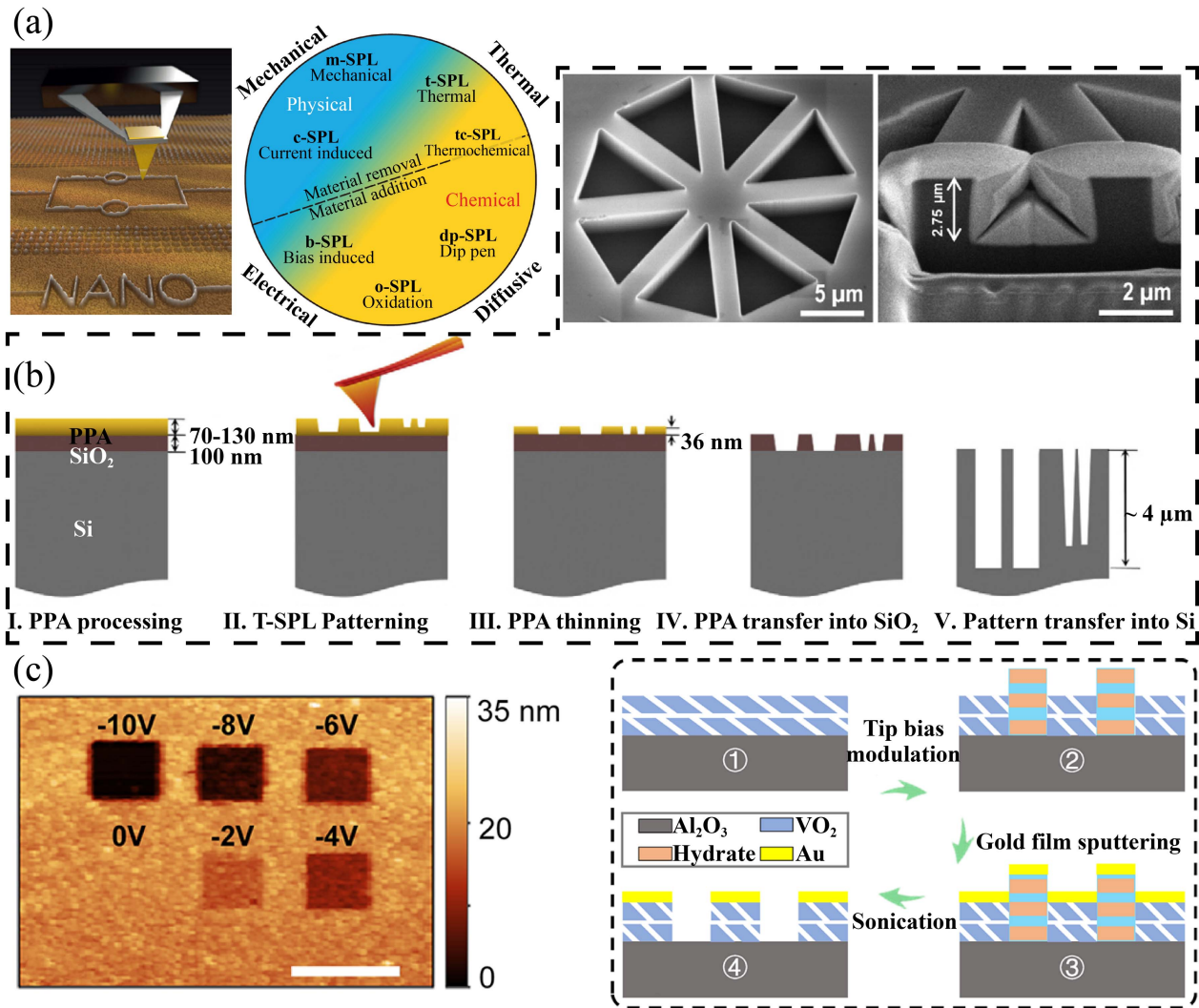




**Fig. 10** The utilization of multistep lithography in the fabrication of all-dielectric metasurfaces. (a) A versatile metasurface with self-cleaning and dynamic color response, fabricated through EBL and laser lithography: Two super wettability states switchable with hydrophobic treatment or O<sub>2</sub> hydrophilic treatment (left).<sup>[238]</sup> Illustration of the multistep fabrication process for the sample (right). (b) Schematic of the fabrication process for the synthetic aperture metalens using EBL and photolithography.<sup>[221]</sup> (c) Hybrid metal-dielectric metasurfaces: Stepwise fabrication process of stacked hybrid nanoantenna metasurfaces (left). SEM images of the fabricated metasurfaces (right).<sup>[222]</sup>

has found applications in semiconductor manufacturing, striking a balance between resolution and cost-effectiveness for specific uses. As illustrated in Fig. 12(c), Park *et al.* presented centimeter-scale nanopillar-based metalenses made of glass

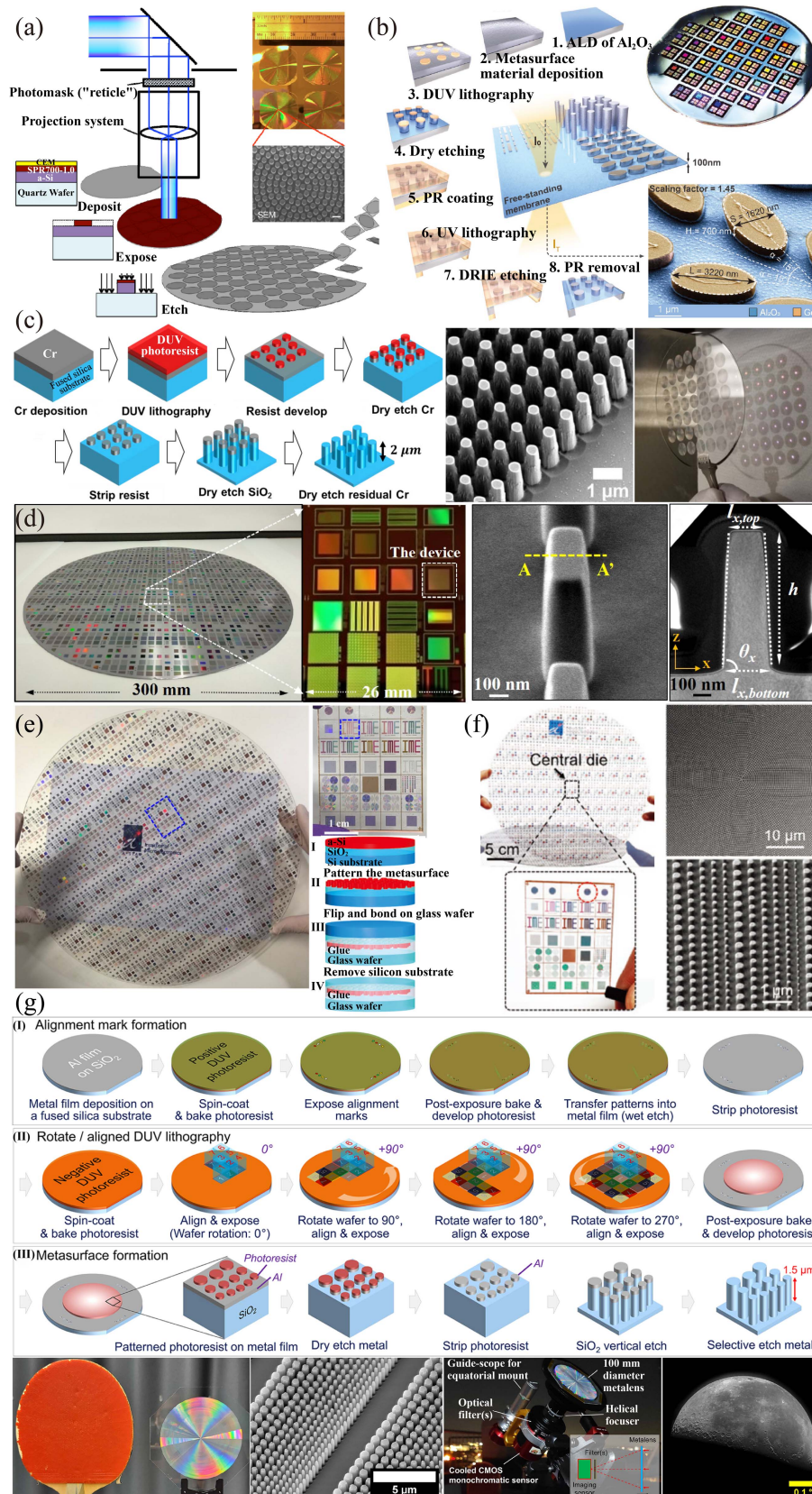
fabricated using KrF DUV lithography, demonstrating the capability of focusing and imaging at visible wavelengths<sup>[268]</sup>. Furthermore, Leitis *et al.* employed KrF DUV lithography to introduce a versatile method for wafer-scale and cost-effective



**Fig. 11** The utilization of scanning probe lithography in the fabrication of all-dielectric metasurfaces. (a) SPL for imaging and patterning applications.<sup>[239]</sup> (b) Nanopatterning achieved through combined thermal SPL and dry etching, along with corresponding SEM images of silicon fabrication using a SiO<sub>2</sub> hard mask.<sup>[240]</sup> (c) Fabrication of a tunable metasurface based on VO<sub>2</sub> using electric-field SPL with precise depth control: Topography image of square patterns modulated by different tip bias voltages and followed by sonication. The scale bar is 2 μm (left). Schematic representation of the main steps in fabricating the designed metasurface (right).<sup>[241]</sup>

fabrication of Ge metasurfaces. This approach facilitates mid-IR photonics and biosensing in a high-throughput manner on silicon wafers, supporting optically transparent free-standing Al<sub>2</sub>O<sub>3</sub> membranes [indicated by the blue color label in the SEM image of Fig. 12(b)]<sup>[269]</sup>. ArF DUV lithography stands as a more sophisticated technology compared to i-line and KrF lithography, proving instrumental in the manufacturing of dielectric metasurfaces with reduced feature sizes. Xu *et al.* showcased the mass production of a Si nanopyramid-based metasurface polarizing bandpass filter on a 12 in. wafer. This achievement was realized through the utilization of 193 nm ArF DUV immersion lithography and direct inductively coupled plasma etching, strategically employed to manipulate polarization in the shortwave IR wavelength range<sup>[270]</sup>. Moreover, metasurface-based subtractive color filters have been manufactured on 12 in. glass wafer substrates utilizing 193 nm ArF DUV

lithography. Through variations in pillar height and pitch, metasurfaces displaying different colors are attained, as illustrated in Fig. 12(e). It is noteworthy that the patterned a-Si layer is transferred from the Si wafer to a glass wafer via a bonding glue-assisted layer transfer process, as depicted in the bottom right of Fig. 12(e)<sup>[100,271]</sup>. They also showcased an a-Si metalens designed for fingerprint imaging operating at 940 nm. This metalens was fabricated on a 12 in. glass wafer using the 193 nm ArF DUV lithography. The enlarged optical image of the central die on the wafer is depicted in Fig. 12(f), highlighting the metalens, which is labeled by a red circle<sup>[272]</sup>. Most recently, Park *et al.* showcased an all-glass metalens with a diameter of 100 mm designed for operation in the visible spectrum, employing KrF DUV projection lithography as illustrated in the top panel of Fig. 12(g)<sup>[136]</sup>. A photograph and a tilted SEM image of the 100-mm-diameter metalens are presented in the bottom left



**Fig. 12** The utilization of UV lithography in the fabrication of all-dielectric metasurfaces. (a) The schematic diagram illustrates the production of large-area metalenses utilizing photolithographic stepper technology. An accompanying photograph of the fabricated metalenses (upper right) shows a 2 cm diameter. A SEM image of the metalens center (center right) reveals the

nanocylinder constituting the metalens. Scale bar: 2  $\mu\text{m}$ .<sup>[267]</sup> (b) The fabrication process of wafer-scale membrane-based metasurfaces for mid-infrared photonics and biosensing. In the top right photograph, a fully processed 4 in. silicon wafer is depicted, showcasing large-area metasurfaces. The bottom right provides a high-magnification SEM image of the metasurface unit cell, revealing the thin  $\text{Al}_2\text{O}_3$  membrane in blue and the Ge resonators in orange.<sup>[269]</sup> (c) The left image illustrates the fabrication process of an all-glass, large metalens at visible wavelengths utilizing DUV projection lithography. On the right, zoomed-in SEM images showcase the nanopillars of the metalens, while a photograph displays the fabricated 1 cm metalenses on a 4 in.  $\text{SiO}_2$  wafer.<sup>[268]</sup> (d) Photograph of the 12 in. silicon metasurface wafer, meticulously fabricated through the Multiple-Projects Wafer (MPW) line at Microelectronics IME for the production of polarizing band-pass filters. A detailed examination of the wafer center is presented in the zoomed-in view (left). Additionally, a tilted SEM image and a cross-sectional TEM image showcase a single silicon pyramid with intricate details (right).<sup>[270]</sup> (e) Image of a metasurface-based subtractive color filter crafted on a 12 in. glass wafer through a CMOS platform. In the top-right section, there are photo images of the central die subsequent to wafer dicing. The bottom-right segment illustrates the schematic of the fabrication process for layer transfer onto the glass wafer.<sup>[100, 271]</sup> (f) The left column depicts CMOS-compatible a-Si metalenses on a 12 in. glass wafer designed for fingerprint imaging, with the central circled region on the wafer highlighting the metalens. The right column showcases SEM images captured at both the central and outer zones of the metalens.<sup>[272]</sup> (g) The fabrication process of an all-glass 100-mm-diameter visible metalens for imaging the cosmos is depicted in the top panel. The bottom-left section features a photograph of the metalens, providing a size comparison with a table tennis racket, along with a SEM image showcasing the fused silica nanopillars constituting the metalens. In the bottom-right section, a photograph of the astro-imager is presented, consisting solely of a 100-mm-diameter metalens, an exchangeable optical filter, and a cooled CMOS monochromatic sensor. Additionally, an acquired image of the Moon is displayed.<sup>[136]</sup>

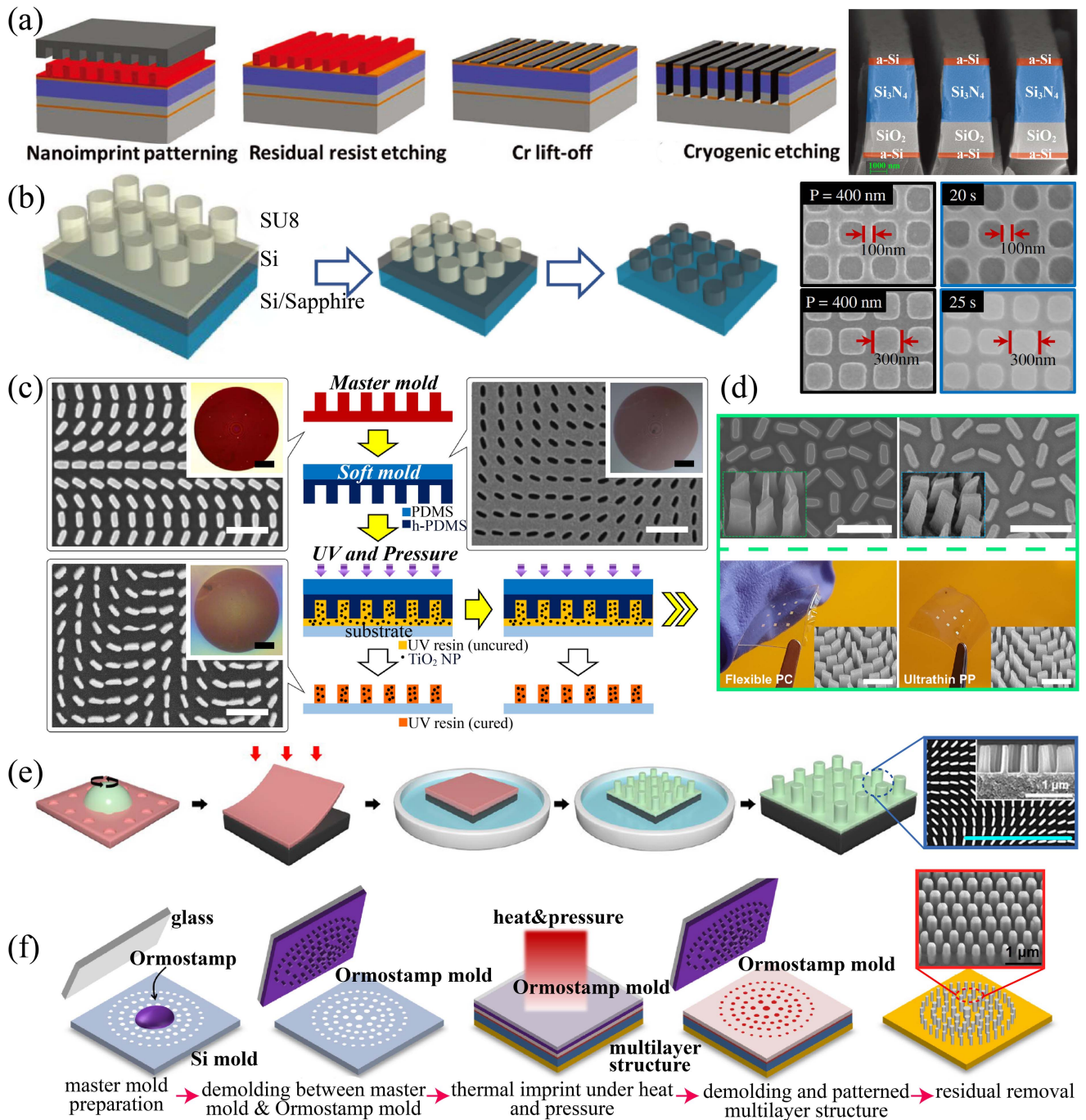
of Fig. 12(g), respectively. Furthermore, they successfully demonstrated direct astronomical imaging of the Moon at visible wavelengths, as depicted in the bottom right of Fig. 12(g).

Each of these lithography techniques signifies a milestone in the evolution of semiconductor manufacturing, with continuous endeavors to extend the boundaries of resolution and enhance the overall efficiency of the metasurface fabrication process.

### 3.3.2 Nanoimprint lithography

Nanoimprint lithography (NIL) has emerged as a pivotal tool in nanofabrication, providing a cost-effective, high-throughput, and large-scale production alternative to traditional photolithography techniques. In contrast to optical lithography, NIL is a nanofabrication technique that involves replicating patterns from a mold onto a resist-coated substrate, typically using a polymer material through physical deformation or curing. There are two main types of NIL based on the curing mechanism: thermal nanoimprint lithography (T-NIL) and ultraviolet nanoimprint lithography (UV-NIL)<sup>[273–276]</sup>. Thermal NIL relies on heating and cooling, wherein the resist material is softened or made molten by heating it to a temperature above its glass transition temperature. The mold is then pressed into the softened resist material, creating a replica of the mold pattern when its temperature is brought below the glass transition temperature. UV-NIL eliminates the need for heating and cooling processes using UV light to cure a UV-sensitive resist material. In comparison to T-NIL, UV-NIL is widely employed in metasurface fabrication due to its higher resolution and faster processing. Moreover, soft NIL, involving flexible molds made of polymeric materials like polydimethylsiloxane (PDMS), has been explored in the NIL process. This method, termed as soft NIL, enables the fabrication of metasurfaces on arbitrary surfaces<sup>[277,278]</sup>. The resist imprints the reverse structure from the original mold, not only serving as the hard mask but also directly functioning as the

optical device. Yao *et al.* fabricated heterogeneous all-dielectric metasurfaces consisting of a-Si,  $\text{Si}_3\text{N}_4$ , and  $\text{SiO}_2$ , acting as an efficient ultra-broadband reflector through the NIL patterning process and etching. In the fabrication process illustrated in Fig. 13(a), the NIL-patterned resist is employed to create a Cr mask. Once the Cr mask is formed, RIE recipes are developed to etch the dielectric materials, resulting in a well-defined etching profile<sup>[279]</sup>. Moreover, the replication and transfer of nanostructured photoresist patterns onto the substrate can function as an etching mask for the final structure. As illustrated in Fig. 13(b), the primary stamp precisely transferred the master's pattern into the SU-8 photoresist. Subsequently, through optimized etching, well-controlled structures on the silicon can be obtained<sup>[280]</sup>. The conventional UV-NIL process typically employs the resist as the sole hard mask. However, to enhance the functional capabilities of the NIL resist, Yoon *et al.* pioneered the development of a  $\text{TiO}_2$  nanoparticle-embedded UV-curable resin, illustrated in Fig. 13(c)<sup>[281]</sup>. Consequently, a dielectric metalens can be directly fabricated with a single step of UV-NIL, eliminating the need for secondary processes such as deposition or etching. In a subsequent investigation, Kim *et al.* introduced a metahologram crafted from a  $\text{TiO}_2$  nanoparticle-embedded resist (PER), achieving a remarkable efficiency record of 90.6%. This metahologram was manufactured using a one-step NIL process, as depicted in Fig. 13(d)<sup>[126]</sup>. Furthermore, this one-step nanoimprinting facilitates transfer onto various substrates, ranging from flexible films to diverse hard surfaces like PC, PP, and curved glasses. To address issues such as warpage and breakage of nanostructures during mold detachment, Choi *et al.* innovatively replaced the detaching process with wet etching of a replica mold crafted from a water-soluble polymer, polyvinyl alcohol (PVA). The fabrication process for wet etching NIL is illustrated in Fig. 13(e)<sup>[282]</sup>.  $\text{TiO}_2$  PER is applied to the PVA replica mold through spin coating. Following the curing of PER under



**Fig. 13** The utilization of nanoimprint lithography in the fabrication of all-dielectric metasurfaces. (a) The fabrication process of an all-dielectric heterogeneous metasurface, designed as an efficient ultra-broadband reflector, is depicted on the left. The cross-sectional SEM image on the right provides a detailed view of the fabricated metasurface.<sup>[279]</sup> (b) Schematic representation of the fabrication process for silicon nanostructures with adjustable geometries through soft nanoimprint lithography and reactive ion etching (left). The initial imprinted SU8 nanostructure is presented in the first column before etching. The second column displays the results after well-controlled etching times, showcasing the tunable meta-atom geometries (right).<sup>[280]</sup> (c) Single-step manufacturing of hierarchical dielectric metalenses in the visible. SEM images depict the master mold, soft mold, and final plum pudding metalens, respectively. Scale bars: 1  $\mu\text{m}$ . Scale bars of inset optical micrographs: 100  $\mu\text{m}$ .<sup>[281]</sup> (d) The TiO<sub>2</sub> nano-PER metahologram is fabricated through a one-step process of nanoparticle-embedded-resin printing. The initial row showcases the SEM image of the master mold meticulously crafted using EBL on a silicon substrate. The subsequent image displays the intricate final structure of the printed TiO<sub>2</sub> nano-PER. In the second row, the successful

replication of the TiO<sub>2</sub> nano-PER metahologram is exhibited on diverse substrates, including flexible polycarbonate (PC) and ultrathin polypropylene (PP). Scale bars: 1 μm.<sup>[126]</sup> (e) Illustration depicting the achievement of high-aspect-ratio metalenses through nanoimprint lithography utilizing water-soluble stamps (left). SEM images showcasing the replicated metalens (right).<sup>[282]</sup> (f) Schematic illustration depicting the fabrication of a metalens through multilayer nanoimprint lithography and solution phase epitaxy. The inset images on the right showcase SEM images of the ZnO metalens.<sup>[283]</sup>

sufficient pressure and UV illumination, the substrate adheres to the PVA mold. As PVA is a water-soluble polymer, the PVA mold covering the substrate dissolves, leaving only the replicated TiO<sub>2</sub> nanostructures on the substrate. This method, unlike conventional NIL, avoids introducing shear stress in cured nanostructures, enabling the replication of various patterns without faults. Moreover, through the utilization of multilayer NIL and solution-phase epitaxy (SPE), high-aspect-ratio ZnO nanopillar-based metalenses are successfully demonstrated<sup>[283]</sup>. The main procedure, as illustrated in Fig. 13(f), involves initially replicating an OrmoStamp mold from the silicon mold. Subsequently, a nanoimprint resist is spin-coated on top of the multilayer structure, patterned by a thermal nanoimprint with the OrmoStamp mold, and followed by dry etching of the SiO<sub>2</sub> layer and the PMMA layer. The PMMA layer is fully etched to expose the ZnO seed layer. Finally, ZnO nanopillars are grown in a precursor solution, and the residual SiO<sub>2</sub> and resist layers are removed by dry etching, resulting in the acquisition of the ZnO metalens.

NIL offers the advantages of high resolution, large-area fabrication, and cost-effectiveness. The reusability of stamps further contributes to a substantial reduction in fabrication costs and environmental impact. However, it is important to note that this method still necessitates the fabrication of a mold using high-resolution equipment.

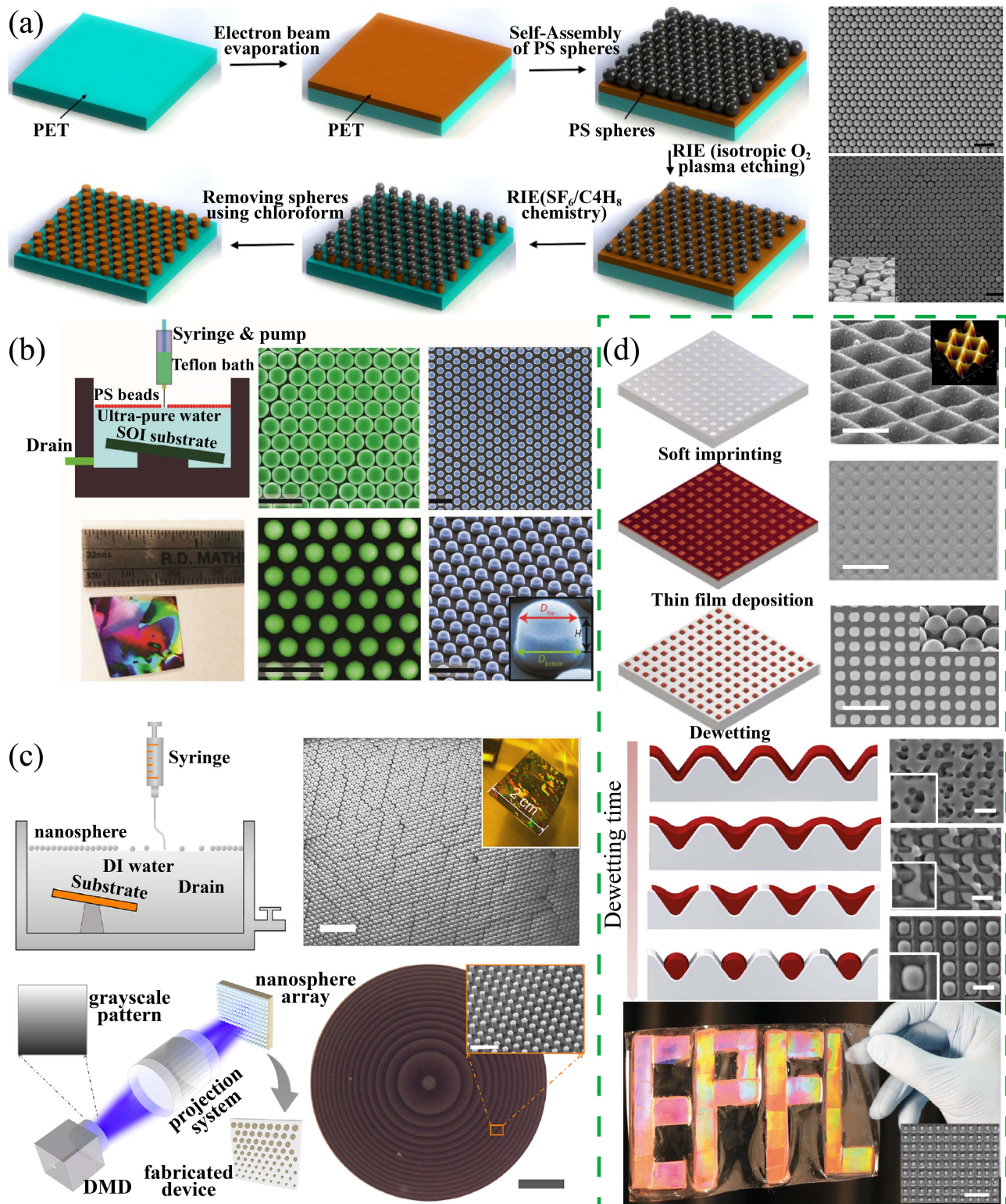
### 3.3.3 Self-assembly of nanostructures

Self-assembly (SA) is a process wherein smaller components spontaneously unite to create larger, well-defined, and stable aggregates<sup>[284]</sup>. Recently, it has emerged as a promising alternative to lithographic techniques for crafting features and structures at the nanometer scale. This approach provides a cost-effective and scalable method to achieve intricate structures, followed by subsequent etching or deposition processes. Self-assembled monolayers (SAMs) consist of single layers of molecules that autonomously organize into ordered lattices on the surface of a substrate. Various SAMs, formed using organic molecules, have found extensive application in the fabrication of dielectric metasurfaces. Zhang *et al.* presented a flexible, all-dielectric metasurface fabricated through nanosphere self-assembly, showcasing its potential applications in sensing<sup>[285]</sup>. The principal fabrication steps are illustrated in Fig. 14(a): initially, a thin layer of Si is deposited onto a polyethylene terephthalate (PET) substrate. Subsequently, a monolayer of polystyrene (PS) spheres self-assembles and is transferred onto the Si surface [top right SEM image in Fig. 14(a)]. Following this, plasma etching is employed to reduce the size of the PS spheres, serving as a mask for a second RIE procedure. This preserves the Si cylinder-based metasurface directly beneath the PS spheres. The SEM image of the Si cylinders is depicted in the bottom right of Fig. 14(a). Moreover, Valentine *et al.* elucidated the design and manufacturing of large-scale perfect reflectors using Si-based cylinder resonators on SOI substrate. The patterning was achieved through nanosphere lithography and RIE as

illustrated in Fig. 14(b)<sup>[98]</sup>. The SEM images, coupled with the optical image, unequivocally showcase the capability of nanosphere lithography to produce high-quality dielectric resonators across a sizable area. In a subsequent investigation, a pioneering manufacturing technique was introduced, leveraging grayscale nanosphere lithography. This method enables the cost-effective and scalable fabrication of metasurfaces featuring arbitrary, nonperiodic phase profiles. To attain nonperiodic phase control, nanosphere lithography is integrated with illumination from an SLM, facilitating spatial control of the dose and exposure size, as illustrated in Fig. 14(c)<sup>[232]</sup>. To assess the efficacy of the technique, they produced large-scale metalenses operating at a wavelength of 1.7 μm. The fabricated metalenses showcased diffraction-limited focusing and achieved an impressive relative efficiency exceeding 83%. Moreover, Gupta *et al.* proposed a straightforward self-assembly fabrication method combined with nanoimprinting approaches, eliminating the need for nanospheres. They showcased the controlled dewetting of optical glass thin films to achieve all-dielectric metasurfaces over a large area. The self-assembly of various optical nanostructures with feature sizes down to ~100 nm and interparticle distances as small as 10 nm is experimentally demonstrated. The fabrication approach is illustrated in Fig. 14(d)<sup>[286]</sup>. Soft lithography is initially employed to imprint a PDMS mask onto a thermoplastic or sol-gel layer through nanoimprinting [Fig. 14(d), top]. Subsequently, a thin layer of chalcogenide glass is deposited through evaporation. The annealing process induces dewetting of the glass layer, leading to its breakup into an array of well-dispersed and self-ordered nanoparticles, as depicted in the SEM images in Fig. 14(d). This approach is both simple and scalable, as underscored in the bottom of Fig. 14(d), where an EPFL logo has been created by embossing a 20 × 11 cm<sup>2</sup> polycarbonate sheet with nanostructured assembled PDMS molds. In addition to the aforementioned large-scale fabrication techniques, researchers have also demonstrated a novel method utilizing an anodized aluminum oxide (AAO) template combined with a TiO<sub>2</sub> metasurface<sup>[287]</sup>. Furthermore, they have explored the direct construction of larger-scale nanostructures on resist<sup>[288–290]</sup>.

## 4 Extreme Fabrication and Application

Through the exploration and optimization of nanofabrication technology, various intricate nanostructures have been proposed through extreme fabrication techniques, encompassing multilayer fabrication<sup>[291–295]</sup>, flexibility<sup>[296–298]</sup>, slanted orientation<sup>[84,94,299,300]</sup>, and high aspect ratios<sup>[301,302]</sup>. These innovative approaches introduce additional degrees of freedom for tailoring metasurface functionalities. The pursuit of extreme fabrication and the application of metasurfaces involve pushing the boundaries of conventional nanofabrication techniques to achieve structures with unprecedented properties and functionalities. The following provides a summary of recent advanced dielectric metasurface nanofabrication methods, covering multilayer to



**Fig. 14** The utilization of self-assembly in the fabrication of all-dielectric metasurfaces. (a) Illustration of the primary fabrication steps for the flexible, all-dielectric metasurface utilizing nanosphere lithography. The accompanying images on the right showcase SEM images of self-assembled polystyrene (PS) spheres (top) and Si cylinders (bottom) formed after RIE. Scale bars: 1  $\mu\text{m}$ .<sup>[285]</sup> (b) Illustration of the self-assembly-based nanosphere lithography technique for the large-scale fabrication of all-dielectric metasurface perfect reflectors, accompanied by a camera image depicting the pattern of PS spheres on an SOI substrate (left column). SEM images showcase PS spheres with a diameter initially at 820 nm and subsequently downscaled to 560 nm

after etching (middle column). The top and tilted views exhibit the final metamaterial, comprising an array of Si cylinders. Scale bar: 2  $\mu\text{m}$ .<sup>[98]</sup> (c) Image of large-scale metasurfaces created through grayscale nanosphere lithography. A grayscale pattern is produced by the DMD system utilizing a 365 nm I-line UV light source, transmitted through a projection system incorporating an objective lens. The schematic depicts the air–water interface for nanosphere self-assembly, while the SEM image showcases the close-packed nanosphere monolayer after self-assembly, with a scale bar of 5  $\mu\text{m}$ . Additionally, an optical image of the fabricated metalens is presented. Scale bar: 200  $\mu\text{m}$ .<sup>[232]</sup> (d) The left panel illustrates the schematic, while the right panel displays the SEM image of the self-assembly process of nanostructured glass metasurfaces through templated fluid instabilities. The fabrication initiates with thermal or ultraviolet nanoimprinting of the requisite pattern on a substrate, as shown in the top section (scale bar, 400 nm). Subsequently, a thin-film deposition of high-index optical glass is performed in the middle section (scale bar, 1  $\mu\text{m}$ ). The final step involves annealing to induce the dewetting process, depicted in the bottom section (scale bar, 1  $\mu\text{m}$ ). A schematic of the dewetting process is also provided. An optical photograph showcases a large area (20 cm  $\times$  11 cm) EPFL logo-shaped metasurface on a polymer substrate, with an inset SEM image revealing the corresponding nanostructure (scale bar, 1  $\mu\text{m}$ ).<sup>[286]</sup>

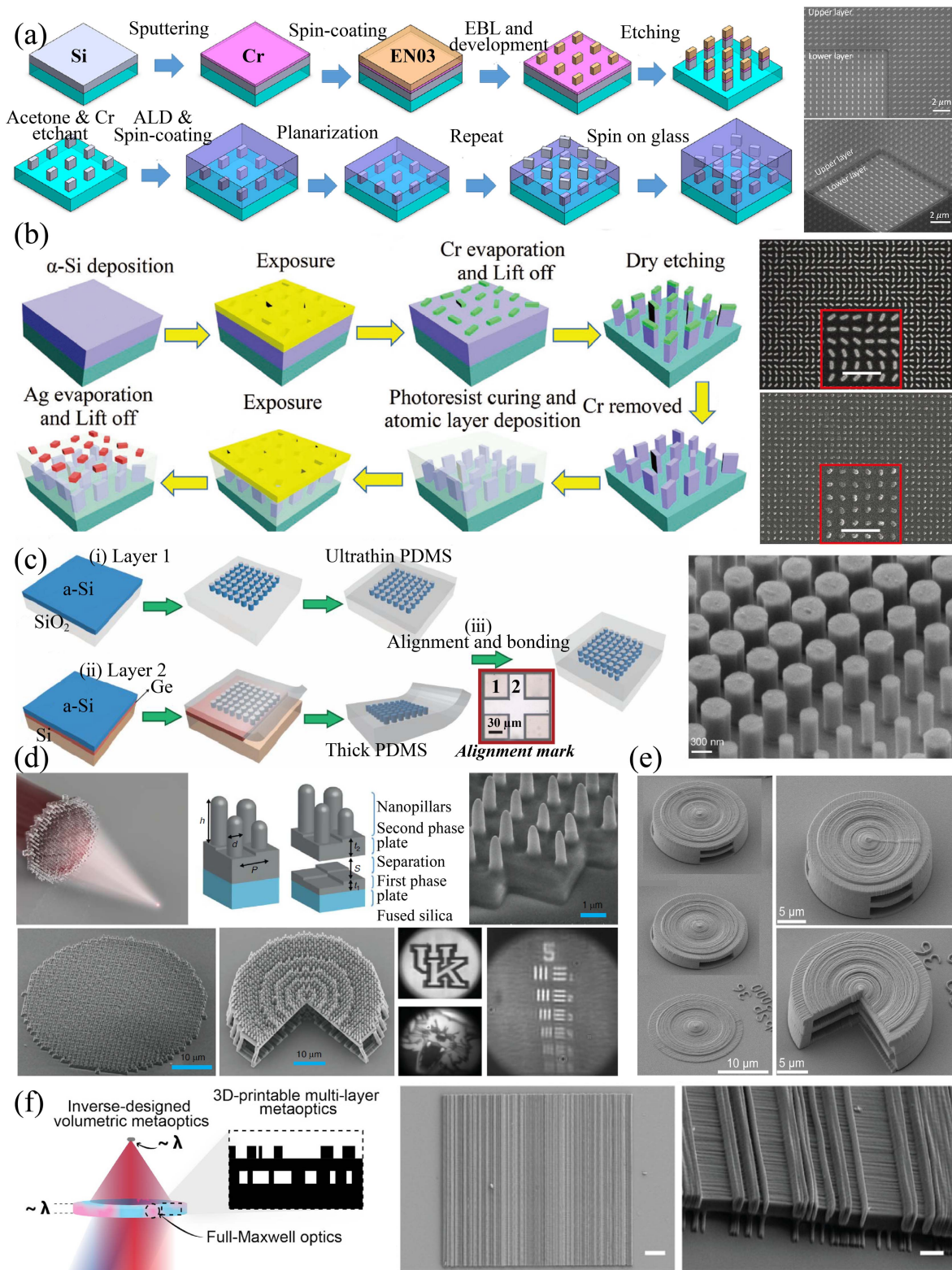
high-aspect-ratio fabrication. These extreme fabrication techniques enhance the capabilities of conventional methods, unlocking new possibilities for optical devices with superior performance and adaptability.

#### 4.1 Multilayer dielectric metasurface nanofabrication

Multilayer dielectric metasurface nanofabrication involves creating dielectric metasurfaces with multiple layers of distinct structures or materials. Fabricating multilayer dielectric metasurfaces typically necessitates multiple lithographic and processing steps, which require precise alignment of the successive layers. Current advanced nanotechnology facilitates the use of lithography tools with a straightforward versatile alignment scheme and procedure. Alignment can be conducted either manually, targeting any visible feature on the surface of the wafer or chip, or automatically, utilizing well-defined and fabricated alignment marks. Moreover, the impact of misalignment on the optical response of the metasurface should be thoroughly considered. Any misalignment between the upper and lower layers within the achieved fabrication tolerances must be accounted to ensure optimal performance. Tanaka *et al.* conducted experimental demonstrations on bilayer chiral dielectric metasurfaces that exhibited multipolar resonances. Notably, they achieved a record-high circular dichroism of 0.7 at that time. The fabrication of bilayer silicon metasurfaces involved a two-step EBL procedure, as illustrated in Fig. 15(a)<sup>[303]</sup>. They used standard nanolithography to fabricate the first silicon metasurface on fused silica. Next, they embedded the silicon metasurface into a planar glass layer by depositing a SiO<sub>2</sub> layer and spin-coating a layer of polymer. To fabricate the second layer of the bilayer system, the previously described lithography process was repeated with precise alignment and etching steps. The top-view SEM and oblique-view SEM of the fabricated bilayer metasurfaces of different arrays are shown in the right SEM images in Fig. 15(a). Researchers have not only fabricated bilayer metasurfaces using identical dielectric materials but have also explored the creation of bilayers composed of different materials. Leveraging precise nanofabrication technology, Deng *et al.* introduced a bilayer metasurface designed to independently manipulate the phase of visible light in both reflection and transmission spaces. This innovative bilayer metasurface comprises two distinct anisotropic nanobricks, namely silicon and silver. The intricate fabrication process of these bilayer

metasurfaces is detailed in Fig. 15(b)<sup>[103]</sup>. In the initial step, the first layer of the  $\alpha$ -Si metasurface is crafted through the EBL and etching process. Subsequently, a negative photoresist film is subjected to exposure via a lithography machine. Following this, an Al<sub>2</sub>O<sub>3</sub> film is deposited to safeguard the photoresist. Finally, the sample undergoes recoating with a PMMA resist, followed by exposure through an overlay EBL procedure. Upon the evaporation of Ag and subsequent lift-off, a bilayer metasurface emerges, comprising arrays of  $\alpha$ -Si and Ag nanobricks. The SEM images of the meta-hologram sample are presented in Fig. 15(b) on the right. In addition to employing two-step lithography for fabricating multilayer metasurfaces, researchers have showcased independent phase modulation at multiple wavelengths by closely stacking dielectric metasurfaces embedded in PDMS. To experimentally validate this approach, a metalens doublet was fabricated through essential steps, as summarized in Fig. 15(c). The initial  $\alpha$ -Si metalens was delineated utilizing EBL and RIE. Subsequently, a thin layer of PDMS was spin-coated to encapsulate the nano-posts, forming a slender spacer layer that enables the close stacking of the second layer. The second substrate underwent preparation involving the deposition of a Ge layer as the sacrificial layer, succeeded by  $\alpha$ -Si growth on Ge. The silicon metalens was crafted through identical procedures, followed by embedding the lens in a substantial PDMS layer. Following the dissolution of the Ge layer for substrate release, the transferred layer was precisely aligned by the alignment marks and bonded to the initial layer. An SEM image of the silicon nano-posts is presented in Fig. 15(c) on the right. Essentially, the aforementioned methods can be extended to fabricate three or even more layers of metasurfaces by iteratively executing the steps of encapsulation and nanostructure fabrication. Furthermore, leveraging advanced 3D printer technology, multilayer nanostructures can be directly fabricated through multiphoton lithography, obviating the necessity for multistep lithography and etching processes. Balli *et al.* introduced a hybrid achromatic metalens using two-photon lithography. These hybrid achromatic metalenses integrate a phase plate and a metalens into a single thin element, as depicted in Fig. 15(d), demonstrating high focusing efficiency and chromatic aberration correction across a broad spectral range from 1000 to 1800 nm<sup>[304]</sup>. Moreover, employing a 3D printer, researchers introduced a novel design for multilayer achromatic metalens and multilayer meta-optics [depicted in Figs. 15(e) and 15(f)]<sup>[305,306]</sup>. They utilized topology optimization and





**Fig. 15** Multilayer dielectric metasurface nanofabrication. (a) A depiction of the sequential steps involved in the fabrication of bilayer chiral silicon metasurfaces. The right image provides a perspective and an oblique-view SEM representation of the accomplished chiral metasurfaces.<sup>[303]</sup> (b) Bilayer metasurfaces for comprehensive light manipulation involve a meticulous fabrication process, as illustrated on the left. The SEM image on the right depicts the a-Si nanobrick array and the Ag nanobrick array, showcasing the intricate details. Scale bars: 1  $\mu\text{m}$ .<sup>[103]</sup> (c) The fabrication process of multilayer non-interacting dielectric metasurfaces for multiwavelength

meta-optics is depicted on the left. The inset displays an optical microscope image of the alignment marks from the two layers. Scale bar: 30  $\mu\text{m}$ . An SEM image of silicon nano-posts before PDMS spin coating is presented on the right. Scale bar: 300 nm.<sup>[307]</sup> (d) The hybrid achromatic metalens, composed of a phase plate and nanopillar, is designed for broadband, near-infrared imaging and fabricated using two-photon lithography.<sup>[304]</sup> (e) Multilayer achromatic metalenses (MAMs) with high numerical aperture produced through 3D printing. The left image presents a tilted view of the fabricated multilayer achromatic metalenses, showcasing deconstructed MAMs with single, double, and triple (full) layers. The right SEM image offers an enlarged view of the full MAM and sectioned MAM.<sup>[305]</sup> (f) Inverse-designed multilayered metaoptics using 3D printing, implemented in a low-index polymer. The SEM of the measured sample is displayed in the right panel.<sup>[306]</sup>

full-wave simulations for the inverse design of multilayer meta-optics, subsequently fabricating the structures in low-refractive-index materials through two-photon polymerization lithography.

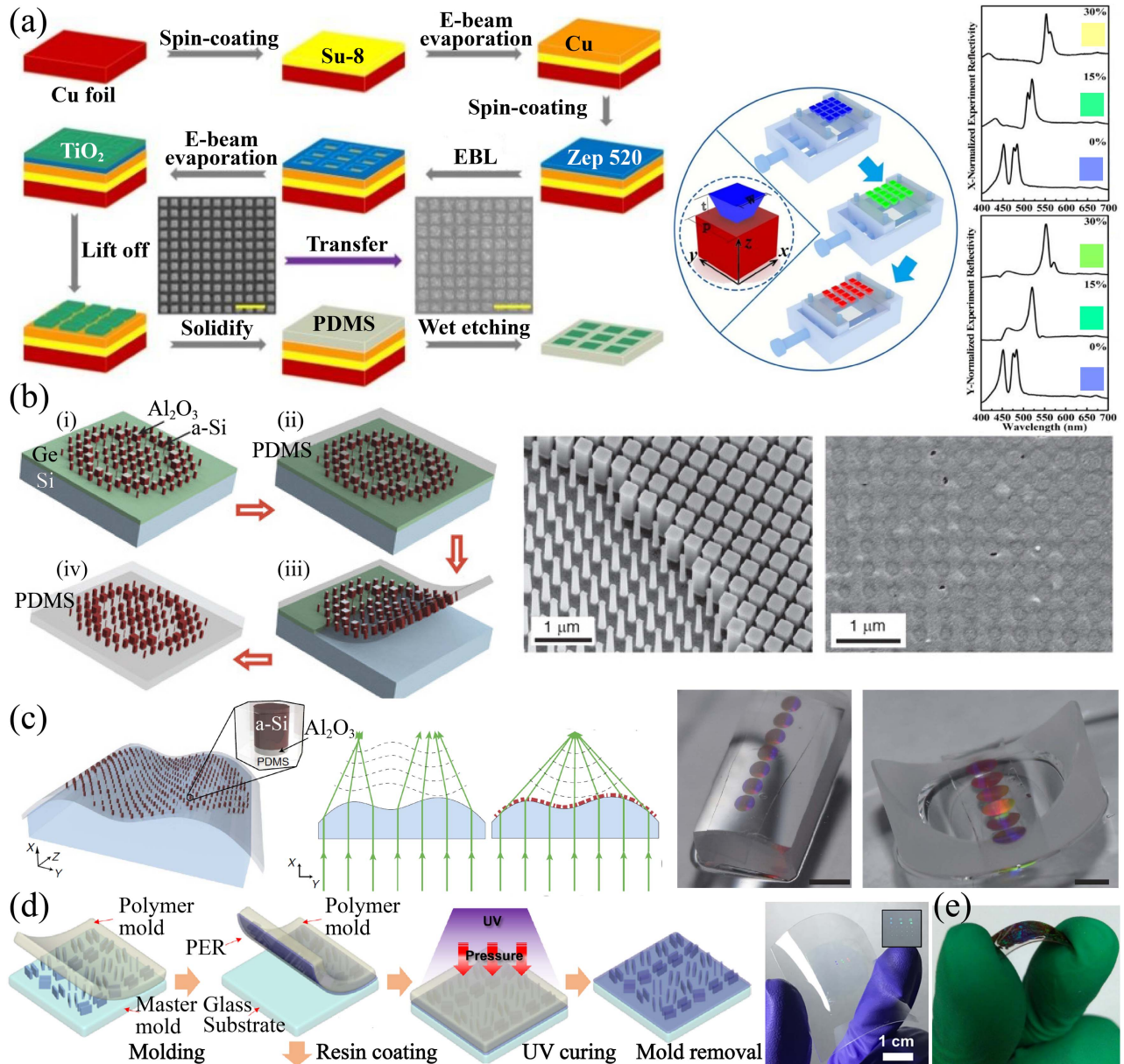
#### 4.2 Flexible dielectric metasurface nanofabrication

Flexible dielectric metasurface nanofabrication is dedicated to the creation of adaptable dielectric metasurfaces capable of conforming to curved or non-planar surfaces. The capacity to bend and stretch metasurface devices on flexible substrates introduces a new dimension to the manipulation of electromagnetic waves, offering the potential for innovative device designs and functionalities. Zhang *et al.* showcased a method for achieving a mechanically tunable all-dielectric metasurface with polarization insensitivity and a full-spectrum response in the visible range [Fig. 16(a)]<sup>[308]</sup>. The structure of the  $\text{TiO}_2$  metasurface is schematically depicted in the middle image of Fig. 16(a). It consists of an array of  $\text{TiO}_2$  nanoblocks embedded in a PDMS substrate. The metasurfaces were fabricated using the EBL technique followed by lift-off and transfer processes, as shown in the left image of Fig. 16(a). An epoxy-based negative photoresist (SU-8) is spin-coated onto the copper foil substrate to decrease the surface roughness of the copper foil. Finally, a Cu/SU-8/Cu substrate serves as a sacrificial substrate for the fabrication and transfer of the  $\text{TiO}_2$  metasurface. Furthermore, researchers demonstrated a highly tunable dielectric metasurface microlens based on silicon nano-posts encapsulated in thin PDMS, showcasing focal distance tuning from 600  $\mu\text{m}$  to 1400  $\mu\text{m}$ <sup>[309]</sup>. Figure 16(b) delineates the essential stages involved in the fabrication of a metasurface. Initially, a Ge sacrificial layer was evaporated onto a silicon wafer. The pattern was crafted using EBL and an etching process. Subsequently, PDMS was spin-coated onto the fabricated metasurface, and the embedded nano-posts within the PDMS were liberated by eliminating the Ge sacrificial layer. The SEM image of the nano-posts before the PDMS spin coating is displayed in the middle of Fig. 16(b), while the SEM image of the nano-posts embedded in PDMS is shown on the right. In a subsequent study, the researchers showcased a method for disentangling the optical properties of objects from their physical shape by employing thin and flexible dielectric metasurfaces [Fig. 16(c)]. These conformal metasurfaces consist of silicon nano-posts embedded in a PDMS substrate, fabricated through a process akin to the one depicted in Fig. 16(b)<sup>[102]</sup>. Conventional flexible substrates are susceptible to plasma damage and organic solvents. The utilization of EBL for fabricating dielectric metasurfaces typically entails plasma processes and organic solvents, rendering direct fabrication on a flexible substrate impractical. Researchers have showcased a

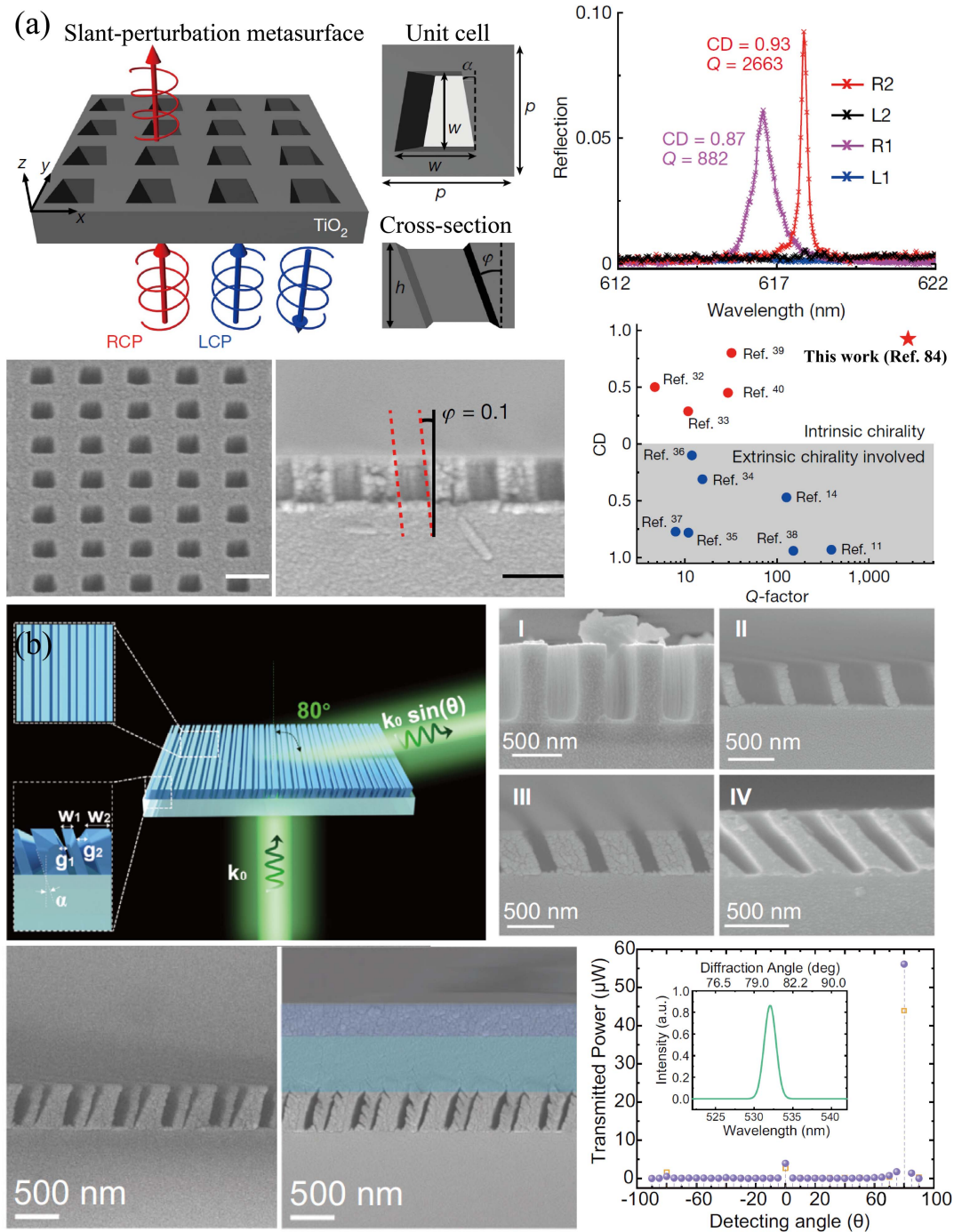
straightforward nanoimprinting method for fabricating dielectric metasurfaces on a flexible substrate [Fig. 16(d), left]<sup>[253]</sup>, a process similar to the one depicted in Fig. 13(c). This approach circumvents the need for plasma processes and organic solvents and is also compatible with flexible substrates [Fig. 16(d) right]. In addition to nanoimprinting, the self-assembly technique can also be employed on flexible substrates, as demonstrated by the metasurface sample on a flexible PET substrate [Fig. 16(e)]<sup>[285]</sup>.

#### 4.3 Slanted dielectric metasurface nanofabrication

Slanted dielectric metasurface nanofabrication involves the fabrication of dielectric metasurfaces with structures oriented at non-perpendicular angles to the substrate. Chen *et al.* introduced a novel paradigm of slant-perturbation metasurfaces and presented the experimental realization of genuine chiral response in resonant metasurfaces. The engineered slant geometry disrupts both in-plane and out-of-plane symmetries, leading to the first observation of intrinsic chiral BICs. This phenomenon exhibits near-unity circular dichroism of 0.93 and a high-quality factor exceeding 2663 for visible frequencies, as illustrated in the right column of Fig. 17(a)<sup>[84]</sup>. The metasurface comprises a square array of slanted trapezoid nanoholes in a  $\text{TiO}_2$  film, positioned on a glass substrate and coated with PMMA [Fig. 17(a)]. This structure evolves from vertical square nanoholes through the introduction of two perturbations: an in-plane deformation angle  $\alpha$  and an out-of-plane slant angle  $\varphi$ , effectively breaking all mirror symmetries. The fabrication of this metasurface is carried out using a modified slanted-etching system, which includes a wedged substrate and an  $\text{Al}_2\text{O}_3$  screen with an aperture serving as an ion collimator. SEM images of the fabricated samples are depicted in the lower left of Fig. 17(a). The utilization of an ion collimator ensures that the left and right sidewalls exhibit nearly identical slant angles. Moreover, Liu *et al.*<sup>[104]</sup> have demonstrated novel slanted metagratings designed for large-angle and high-efficiency anomalous refraction in the visible spectrum. Through the introduction of out-of-plane asymmetry, the study reveals that meta-atoms with simple configurations exhibit comparable degrees of freedom as their topologically optimized counterparts. As a result, the slanted  $\text{TiO}_2$  metagratings can effectively steer light to  $80^\circ$  with impressive relative efficiency (98.5%) and absolute efficiency (89.5%). Figure 17(b) illustrates the schematic of a  $\text{TiO}_2$  metagrating, consisting of  $\text{TiO}_2$  waveguides incorporating two types of asymmetries. In-plane asymmetry is introduced through the utilization of a conventional asymmetric dimer [top inset in Fig. 17(b)], strategically designed to govern intensity distributions among distinct diffraction orders. Simultaneously, out-of-plane asymmetry is achieved by tilting the sidewalls of gratings



**Fig. 16** Flexible dielectric metasurface nanofabrication. (a) Stretchable all-dielectric metasurfaces with polarization-insensitive and full-spectrum response are depicted in this study. The sample fabrication process is schematically illustrated on the left. The middle graphic portrays the conceptual image of the stretchable TiO<sub>2</sub> metasurface. The SEM images of the TiO<sub>2</sub> metasurface after the lift-off and transfer are provided as insets on the right. Scale bar: 1  $\mu\text{m}$ . The reflection spectra as a function of strain. The insets show corresponding dynamic colors.<sup>[308]</sup> (b) The fabrication of highly tunable elastic dielectric metasurface lenses involves several key steps, as depicted in the schematic on the left. SEM images on the right provide a detailed view of the nano-posts before spin coating the initial PDMS layer and after they are embedded in PDMS.<sup>[309]</sup> (c) Decoupling the optical function from the geometrical form is achieved through conformal flexible dielectric metasurfaces. The schematic depicts a dielectric metasurface layer conforming to the surface of a transparent object with arbitrary geometry. The side view illustrates a thin dielectric metasurface layer altering its optical response to a desired one (middle). Optical images showcase two flexible metasurfaces conformed to a convex glass cylinder and a concave glass cylinder (right). Scale bar, 2 mm.<sup>[102]</sup> (d) The schematic on the left illustrates the nanoimprint procedure of dielectric metasurfaces with sub-100-nm resolution. On the right, the replication of the metasurface onto a flexible substrate.<sup>[253]</sup> (e) Flexible, all-dielectric metasurface fabricated via nanosphere lithography.<sup>[285]</sup>



**Fig. 17** Slanted dielectric metasurface nanofabrication. (a) Detection of intrinsic chiral bound states in the continuum (BICs). Schematic representation of the slant-perturbation metasurface designed to achieve intrinsic chiral BICs. Side-view (left) and cross-sectional (right) SEM images showcasing the fabricated metasurface. Scale bar: 300 nm. Reflection spectra measurements for two metasurface samples under LCP and RCP incidence, respectively (top right). Comparison of CDs and  $Q$ -factors between some typical experimental works and this work (right bottom).<sup>[84]</sup> (b) The illustration presents slanted TiO<sub>2</sub> metagratings designed for large-angle, high-efficiency anomalous refraction in the visible spectrum. The top left depicts the schematic of the slanted TiO<sub>2</sub> metagrating. In the top right, side-view SEM images showcase TiO<sub>2</sub> gratings with varying etching angles. The bottom section displays the improved TiO<sub>2</sub> metagrating, with SEM images depicting the structure before and after the re-deposition of the SiO<sub>2</sub>/TiO<sub>2</sub> bilayer on the left. The right-bottom section provides experimentally recorded angle-dependent power distributions passing through the metagrating before (open squares) and after (dots) the redeposition.<sup>[104]</sup>

at an angle represented by  $\alpha$  [bottom inset in Fig. 17(b)]. The lower left section of Fig. 17(b) presents side-view SEM images displaying the etched TiO<sub>2</sub> gratings with parallel sidewalls. This achievement is attributed to the utilization of a specially crafted angled aluminum holder featuring a tilting angle for sample mounting. Through the combined impact of this holder and a top aluminum net, the undesirable bending of the effective electric field near the sample surface has been successfully mitigated. This innovative setup facilitates the fabrication of high-quality slanted TiO<sub>2</sub> nanostructures. The lower right portion of Fig. 17(b) showcases tilt-view and side-view SEM images of the fabricated metagratings designed for anomalous refraction. Leveraging optimized etching and deposition technologies, a green laser has been deflected to an impressive 80° with a remarkably high absolute efficiency and relative efficiency of 83.6% and 94.0%, respectively, as depicted in the lower right section of Fig. 17(b). This methodology for creating slanted nanostructures represents a promising avenue in the realm of meta-devices and is poised to catalyze the development of new classes of high-performance nanophotonic systems.

#### 4.4 High-aspect-ratio dielectric metasurface nanofabrication

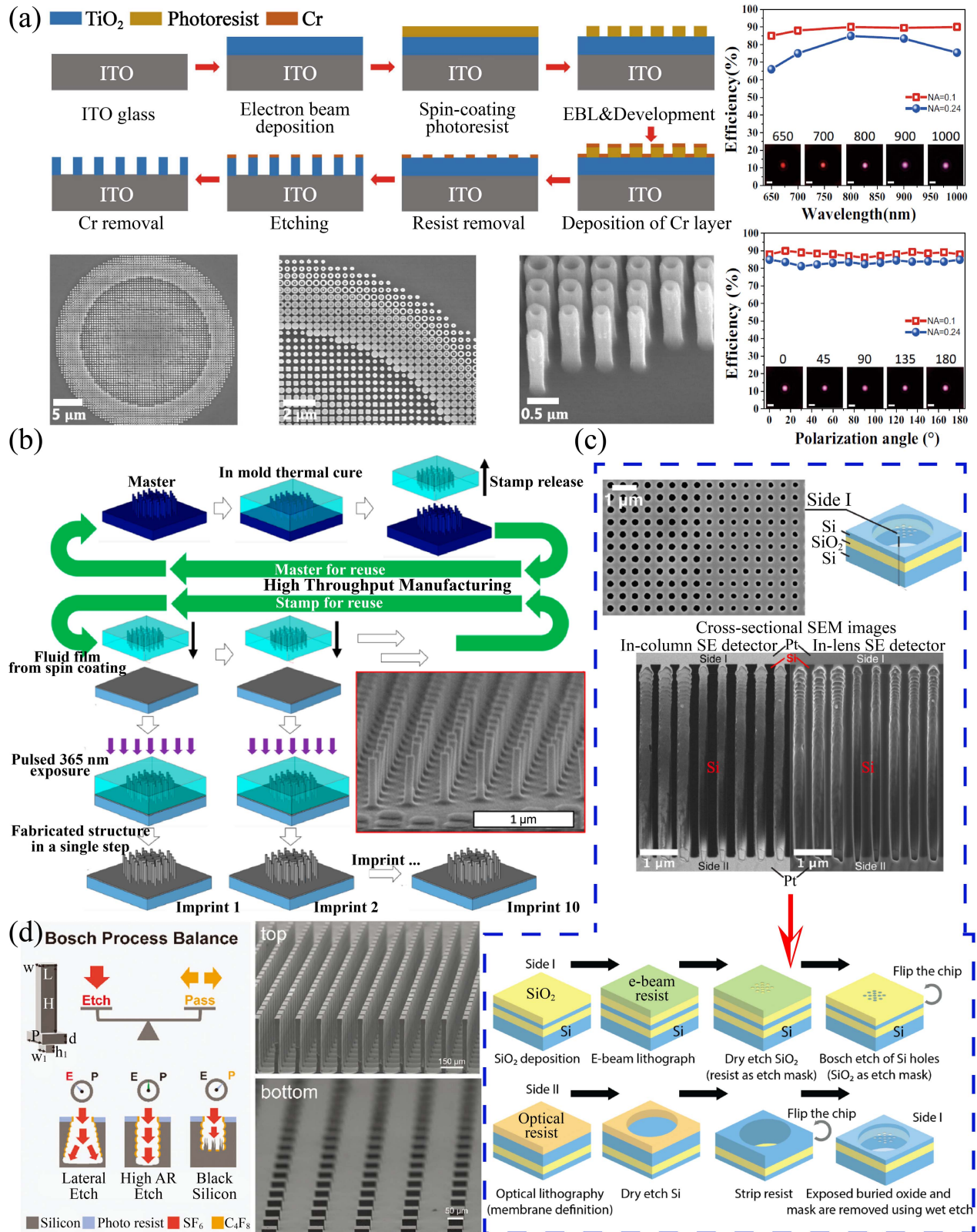
High-aspect-ratio dielectric metasurface nanofabrication is dedicated to achieving high aspect ratios in dielectric structures, thereby enhancing their structural integrity and optical performance. While TiO<sub>2</sub> is widely employed in dielectric metasurface design and fabrication, most TiO<sub>2</sub> metasurface fabrication techniques rely on ALD, which is relatively slow and imposes limitations on the thickness and aspect ratio, typically below 600 nm and 15, respectively. Although the RIE process for TiO<sub>2</sub> has been under study for numerous years, the prior recipes only yield a sidewall with a tilt angle of <75°, significantly restricting applications in high-quality flat devices such as metalenses. In response to these challenges, Wang *et al.* introduced a top-down etching-based technology designed to create TiO<sub>2</sub> nanopillars with unprecedentedly high aspect ratios. This technology yields pillar heights of 1.5  $\mu\text{m}$  and achieves approximately 90° vertical sidewalls. The researchers conducted experiments to demonstrate the effectiveness of this approach, showcasing a polarization-insensitive, broadband TiO<sub>2</sub> achromatic metalens tailored for applications in NIR biological imaging<sup>[101]</sup>. The demonstrated metalens exhibits that the spectral range of achromatism is substantially extended to the wavelength range of 650–1000 nm with an average efficiency of 77.1%–88.5% and a numerical aperture of 0.24–0.1 [right column of Fig. 18(a)]. Illustrated in the left image of Fig. 18(a), the fabrication process involves depositing a 1500-nm-thick TiO<sub>2</sub> membrane through E-beam evaporation, followed by EBL and RIE processes, to create TiO<sub>2</sub> nanostructures. The bottom SEM image of TiO<sub>2</sub> metalenses in Fig. 18(a) reveals that the nanopillars possess nearly perfect vertical sidewalls, with the measured tilt angle of sidewalls around 89°–90°. Additionally, Einck *et al.* presented a one-step additive manufacturing process for the fabrication of metalenses designed for visible wavelengths. This innovative approach utilizes NIL and a TiO<sub>2</sub> nanocrystal-based imprint material<sup>[310]</sup>. Nanostructures with aspect ratios exceeding 8 and critical dimensions smaller than 60 nm were successfully manufactured. The direct-imprint NIL process is illustrated in Fig. 18(b). It begins with the thermal curing of PDMS, after which the stamp is detached from the master and

utilized directly for imprinting. Subsequently, metalenses are formed by imprinting a solution of TiO<sub>2</sub> nanocrystals and curing it using pulsed UV. Following UV curing, the stamp can be released. Cross-sectional SEM images in the inset of Fig. 18(b) reveal aspect ratios of up to 7.8 for the imprinted nano-posts. The predominant building blocks in metasurfaces, in general, are free-standing nanopillar meta-atoms. However, researchers have introduced an alternative metasurface geometry based on high-aspect-ratio via-holes, as illustrated in Fig. 18(c)<sup>[311]</sup>. They designed and characterized metalenses incorporating ultradeep via-holes in 5- $\mu\text{m}$ -thick free-standing silicon membranes, achieving hole aspect ratios approaching 30:1. The fabrication process, as depicted in the bottom image of Fig. 18(c), utilizes EBL and RIE to pattern a SiO<sub>2</sub> hard mask on the device layer surface of a SOI chip, forming the desired holey metalens pattern. A deep Bosch process is subsequently employed to etch high-aspect-ratio holes into the device layer using the hard mask. SEM images of the sidewalls are displayed at the top of Fig. 18(c), while the cross-section of the fabricated metalens is presented in the middle of Fig. 18(c), demonstrating a tilt of the hole wall within 1°. Moreover, utilizing high-aspect-ratio silicon micropillars that exhibit wide transmission-phase tuning ranges with high transmission amplitudes, Wang *et al.* devised and manufactured a series of silicon metasurfaces to achieve efficient spin-multiplexed wavefront controls on THz waves<sup>[312]</sup>. The meta-atom is structured to include three layers [refer to the inset in Fig. 18(d)]: a high micro-pillar, a thick continuous substrate, and a micro-resonator. They constructed a metasurface comprising a periodic array of the designed meta-atoms, illustrated in Fig. 18(d), employing photolithography and deep RIE through the Bosch process on a thick silicon wafer. The desired patterns were transferred onto the wafer using the Bosch process, a time-multiplexed deep etching technique involving both etch and passivation phases. After carefully balancing these two phases, they successfully manufactured the desired metasurface, featuring high-aspect-ratio (approximately 20:1) microstructures with smooth surfaces, straight sidewalls, and precise alignment. The right column of Fig. 18(d) displays the top-view and bottom-view SEM images of the fabricated sample, respectively.

High-aspect-ratio nanostructures have also been demonstrated in GaAs<sup>[57]</sup> metagratings integrated on vertical-cavity surface-emitting lasers and HfO<sub>2</sub><sup>[144]</sup> metasurface optics down to the DUV region, among others.

## 5 Perspective and Outlook

In the past decade, metasurfaces, particularly dielectric metasurfaces, have experienced rapid development due to their lightweight and compact characteristics, driven by low absorption, low impedance, and absence of heat generation. The significant potential of high-performance dielectric metasurfaces lies in their ability to replace bulky optical elements, particularly metalenses in imaging systems. Researchers have developed highly efficient metalenses for the visible spectrum (e.g., GaN with efficiencies ranging from 50.6% to 91.6%)<sup>[151]</sup> and the NIR region (e.g., TiO<sub>2</sub> with efficiencies between 77.1% and 88.5%)<sup>[101]</sup>. Furthermore, using conventional chip fabrication methods such as DUV lithography, wafer-scale metalenses can be manufactured<sup>[268,324]</sup>. Compared to traditional optical element manufacturing, thousands of metalenses can be produced in a single batch, offering scalability and reduced costs. These metasurfaces also significantly reduce thickness and weight, resulting in



**Fig. 18** High-aspect-ratio dielectric metasurface nanofabrication. (a) The diagram illustrates the fabrication process of a high-efficiency broadband achromatic metalens designed for near-infrared biological imaging. A highly directional etching process is employed to create TiO<sub>2</sub> nanostructures. The bottom-left images showcase SEM images of the achromatic metalens with varying resolutions, while the images on the right depict the focusing efficiencies of the developed TiO<sub>2</sub> metalenses.<sup>[101]</sup> (b) The nanoinprint lithography process is employed in the manufacturing

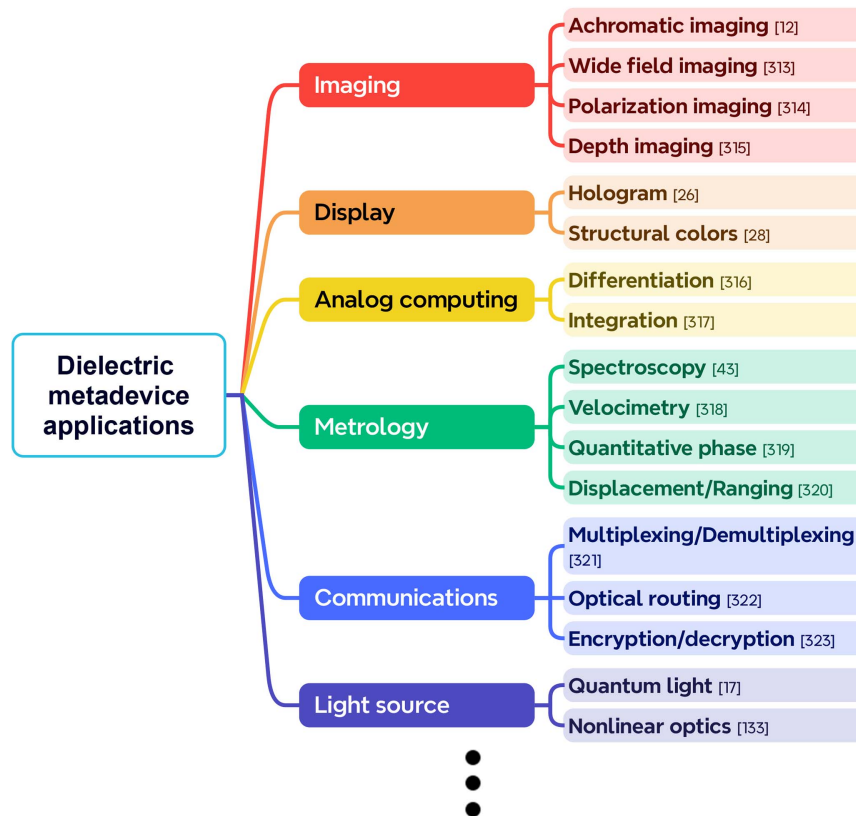
of high-aspect-ratio TiO<sub>2</sub> meta-atoms.<sup>[310]</sup> (c) A high-aspect-ratio inverse-designed holey metalens is depicted in the image. The top section displays an SEM image on the side of the holey metalens. In the middle, cross-sectional SEM images offer a detailed view of a holey metalens. The bottom section outlines the fabrication workflow employed for the production of holey metalenses.<sup>[311]</sup> (d) Bifunctional manipulation is achieved through high-aspect-ratio dielectric metasurfaces. High-quality tri-layer meta-atoms with high aspect ratios (as shown in the inset) are produced using the Bosch process. The achievement of a high-aspect-ratio etch is only viable under these balanced conditions (depicted on the left). The top-view and bottom-view SEM images illustrate the fabricated metasurface (right).<sup>[312]</sup>

more compact and lightweight optical systems. Additionally, metalenses can be fabricated directly on top of CMOS chips due to their flat surfaces<sup>[325]</sup>. The integration based on commercial CMOS process will reduce production costs and improve yield. Figure 19 illustrates further advanced applications of dielectric metasurfaces. More detailed reviews about the applications of metasurfaces across various domains can be found in previous literature<sup>[326–329]</sup>. This miniaturization advantage of meta-devices has led to notable advancements in various applications, including aerial<sup>[330]</sup>, terrestrial<sup>[331]</sup>, and underwater<sup>[318,332]</sup> environments, particularly benefiting modern consumer optoelectronic devices such as augmented reality (AR), virtual reality (VR), and unmanned aerial vehicles<sup>[333]</sup>, among others. In contrast to digital optoelectronic devices, there is considerable interest in optical analog signal processing due to the rising demand for extensive real-time data processing in the era of big data. For instance, one analog image processing method, edge detection based on dielectric metasurfaces, has been extensively

studied in both classical optics<sup>[334–337]</sup> and quantum optics<sup>[338]</sup> due to its low power consumption and high-speed properties. In the realm of metasurfaces, materials serve as the cornerstone, manufacturing techniques form the framework, and applications constitute the outcomes. New materials are yet to be developed, which will pave the way for the advancement of additional manufacturing techniques based on these materials, thereby leading to the development of further applications.

### 5.1 Dielectric materials

This review explores the existing high-index dielectric materials and their associated nanofabrication technologies for high-performance applications. However, optical properties of meta-devices are still limited by the trade-off between refractive index and optical absorption. Further advancements in material science, particularly the creation of advanced dielectric materials exhibiting low loss and high refractive index across a broad



**Fig. 19** Advanced applications, such as imaging<sup>[12, 313–315]</sup>, display<sup>[26, 28]</sup>, analog computing<sup>[316, 317]</sup>, metrology<sup>[43, 318–320]</sup>, communications<sup>[321–323]</sup>, and light sources<sup>[17, 133]</sup> utilizing dielectric meta-devices, have emerged in the past years.

spectrum from UV to IR wavelengths, hold promise for enhancing metasurface performance. Furthermore, the emergence of multifunctional dielectric materials with tunable optical properties, including phase-change materials such as germanium antimony telluride ( $\text{Ge}_2\text{Sb}_2\text{Te}_5$  or GST)<sup>[339]</sup>, vanadium dioxide ( $\text{VO}_2$ )<sup>[340]</sup>, and antimony trisulfide ( $\text{Sb}_2\text{S}_3$ )<sup>[341]</sup>; electro-optic materials like lithium niobate ( $\text{LiNbO}_3$ )<sup>[342]</sup>; and thermo-optic materials like silicon (Si)<sup>[343]</sup> and liquid crystals<sup>[344]</sup>; as well as electrical properties, such as ferroelectric materials like barium titanate ( $\text{BaTiO}_3$ )<sup>[345]</sup> and graphene<sup>[346]</sup>; magnetic properties, such as multiferroic materials like bismuth ferrite ( $\text{BiFeO}_3$ )<sup>[347]</sup>; and magneto-optic materials like yttrium iron garnet (YIG)<sup>[348]</sup>, along with intensity-modulated nonlinear materials like metallic quantum wells<sup>[349–351]</sup>, offers versatile options for metasurface fabrication and functionality enhancement. The combination of tunable properties in these materials has shown promising applications in dynamic, reconfigurable, and high-performance dielectric metasurfaces, including dynamic holograms<sup>[352]</sup>, tunable imaging<sup>[353–355]</sup>, active beam steering<sup>[356]</sup>, and chirality tuning in several recent studies<sup>[341]</sup>. Simultaneously, hybrid structures that combine two or more distinct dielectric or metal materials allow for leveraging the advantages of both plasmonic and all-dielectric elements. For instance, they offer the strong field enhancement of plasmonic resonators along with the low-loss radiation channels of dielectric resonators, presenting an innovative approach to various nanophotonic applications. Compared with pure metallic and all-dielectric metasurfaces, the hybrid metasurface can expand the degrees of freedom for full-space light manipulation<sup>[103]</sup>, optimize and enhance nonlinear light–matter interactions<sup>[357]</sup>, engineer the meta-atom with a range of electromagnetic modes through the interplay between plasmon polaritons (SPP), MDs, and electric dipoles<sup>[222,292,358]</sup>, and provide the high-performance multifunctionality for biosensors<sup>[359,360]</sup> and tunable optoelectronic devices<sup>[361]</sup>. Importantly, artificial-intelligence-driven design techniques can assist in overcoming the limitations of material properties in specific areas. We expect that advancements in high-index dielectric materials, characterized by high electro-optic coefficients, intrinsic nonlinearities, wide transparency windows, and compatibility with nanofabrication technologies, along with a deeper understanding of design principles, will facilitate the development of highly efficient optical devices and unlock new on-chip functionalities.

### 5.2 Nanofabrication techniques

The manufacturing processes of metasurfaces encompass various techniques, including standard nanolithography, advanced nanolithography, large-scale nanolithography, and extreme fabrication methods. Standard nanolithography, including EBL/FIB, remains pivotal in demonstrating innovative meta-optics concepts with intricate structures at an extreme scale level. It is essential for achieving breakthroughs in laboratory-based state-of-the-art fabrication techniques, such as multistep and grayscale lithography. However, advanced nanolithography alone is inadequate for commercial mass production. Consequently, researchers have developed alternative large-scale nanofabrication methods for dielectric metasurfaces, such as self-assembly, NIL, and DUV lithography. The favorability toward adopting automated wafer-scale lithography equipment, notably DUV scanners/steppers, has grown significantly, driven by their acknowledged cost-effectiveness and exceptional

productivity. However, their spatial resolution is insufficient for defining the minute critical dimensions of visible metasurfaces operating at short wavelengths. Additionally, the fabrication expertise for DUV is currently limited to only a few research groups worldwide, which hinders the advancement of this promising technology. NIL, leveraging high-resolution master molds, represents a green wafer-scale lithography technology that offers high pattern fidelity and design flexibility. Exploring innovative processes, such as combining different preparation methods like photolithography and pattern transfer using master mold methods, holds promise for facilitating the preparation of more intricate nanostructures. This approach offers the potential for achieving more efficient and controllable nanostructure preparation.

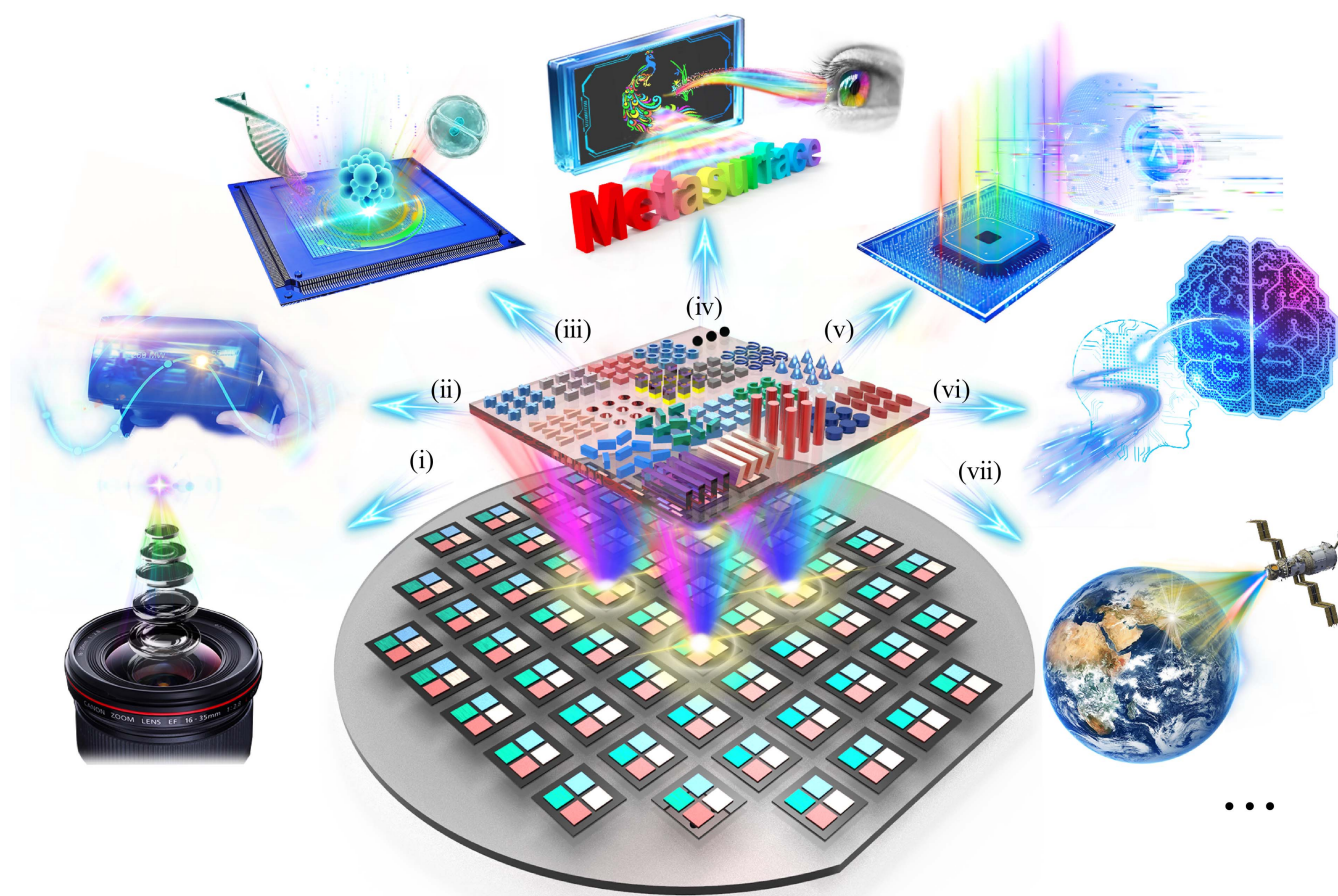
The selection of a fabrication method depends on specific application requirements, including the desired pattern, resolution, and scalability. Each technique offers distinct advantages and is well-suited for particular nano-patterning tasks. Furthermore, optimizing existing nanofabrication methods to enhance production efficiency and material utilization and to reduce preparation costs remains the gold standard for metasurface fabrication. Undoubtedly, these advancements will propel metasurfaces toward widespread commercial applications and facilitate their broader accessibility within the academic and industrial communities.

### 5.3 Emerging technology and field

The journey from research to market represents one of the most promising emerging platforms for meta-optics, owing to its unique set of compelling properties: cost reduction, scalable manufacturing, reproducibility, and long-term stability of the technology, as well as the potential for compact integration of numerous components onto millimeter-sized chips. While some of the dielectric materials and nanofabrication technologies discussed in this review are still primarily in the realm of scientific research rather than serving as reliable commercial platforms for metasurfaces, there exists potential for advancement. With the collective effort of the scientific and industrial communities, we can envision that the establishment of a comprehensive, high-throughput, and cost-effective supply chain covering every aspect from materials to devices will facilitate the widespread integration of dielectric metasurfaces into commercial applications and fully unlock their potential.

Moreover, the transition from chip demonstration to wafer-scale production, or from wafer-scale production to the application of large-scale, small-volume chips, will emerge as the prevailing trend in the metasurface field. We believe that high-performance all-dielectric meta-devices/optics will emerge as a promising option for constructing near-future photonic platforms and will become a major driving force across various fields in the future, owing to the advancements in wafer-scale mass fabrication, as depicted in Fig. 20. These fields include display, optical sensors, aerospace, and others. For example, silicon metasurfaces with a high refractive index have demonstrated an increased gamut to around 181.8% of sRGB, 135.6% of Adobe RGB, and 97.2% of Rec.2020 (UHDTV)<sup>[28]</sup>. Combined with liquid crystals,  $\text{TiO}_2$  metasurfaces have presented high-resolution (pixel size  $\sim 1 \mu\text{m}$ ) SLMs<sup>[362]</sup>. These advancements provide opportunities to develop the next generation of high-resolution, energy-efficient display technologies. Similarly, in optical sensor applications, particularly within the





**Fig. 20** Prospects for the bright future of all-dielectric metadevices/optics with wafer-scale mass fabrication, including (i) metalenses, (ii) AR and VR, (iii) optical sensors, (iv) color/holographic displays, (v) AI-based spectrometer/advanced imaging, (vi) telekinesis and telepathy, and (vii) satellite communications/LIDAR.

biosensor field, high- $Q$  resonant a-Si-based dielectric metasurfaces combined with hyperspectral imaging have demonstrated an ultrasensitive label-free analytical platform for biosensing<sup>[47]</sup>. It is anticipated that the integration of dielectric metasurface-based optical sensor techniques with artificial intelligence (AI) will further enhance the capabilities and efficiency of detection. Furthermore, on-chip devices based on metasurfaces will facilitate the development of highly integrated and intelligent sensor platforms. When dielectric metasurfaces are combined with microfluidic technologies, integrated microfluidic chips gain the ability to manipulate both light and liquids, leading to significant advancements in biosensors<sup>[53]</sup> and display technologies<sup>[363]</sup>. Efficiently coupling light into and out of the metasurfaces within microfluidic channels is crucial and necessitates careful design of optical pathways, along with consideration of the refractive index matching between the metasurface, fluid, and substrate. Integrating metasurfaces into compact microfluidic devices without degrading their optical performance remains a significant challenge. Meanwhile, in the aerospace field, smart metasurfaces incorporating gyroscope sensors have shown potential for satellite communications<sup>[364]</sup>. Additionally, metasurface antennas for small satellites hold promise for providing high-speed data transmission and reception capabilities, reducing the size and weight of satellite communication systems, and lowering launch costs. These examples merely scratch

the surface of the potential applications of metasurface technology. As technology progresses and innovates, the application domains of dielectric metasurfaces, particularly those fabricated on a wafer-scale, are anticipated to expand and deepen. This trend is expected to yield further surprises and breakthroughs across various fields.

## Conflict of Interest

The authors declare no conflict of interest.

## Acknowledgments

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